

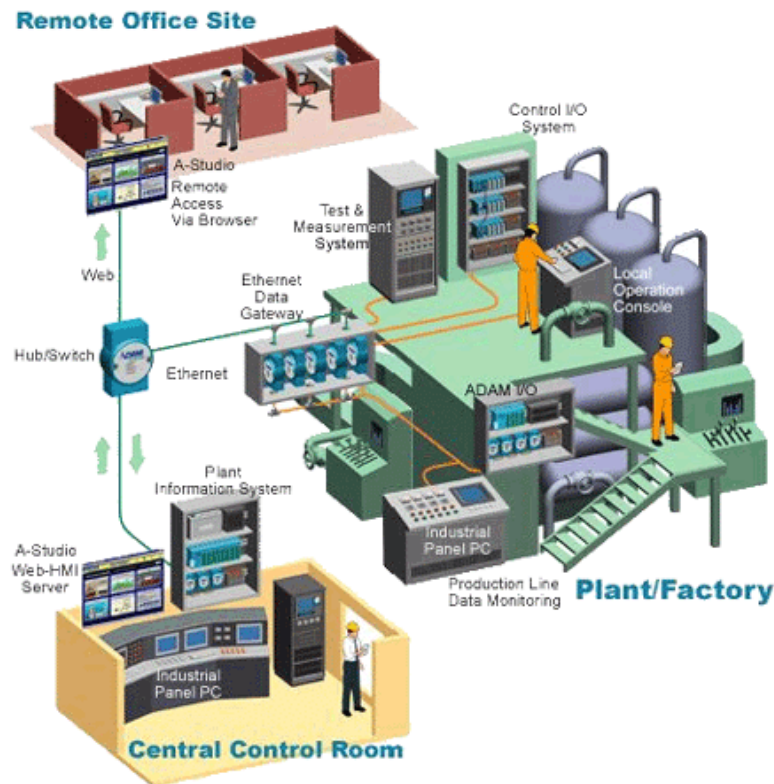
Legacy-Compliant Data Authentication for Industrial Control System Traffic

John Henry Castellanos, Daniele Antonioli, Nils Ole Tippenhauer and Martín Ochoa
Singapore University of Technology and Design

15th International Conference on Applied Cryptography and Network Security
Japan, Kanazawa, July 11, 2017.

Industrial Control Systems

What are ICSs?



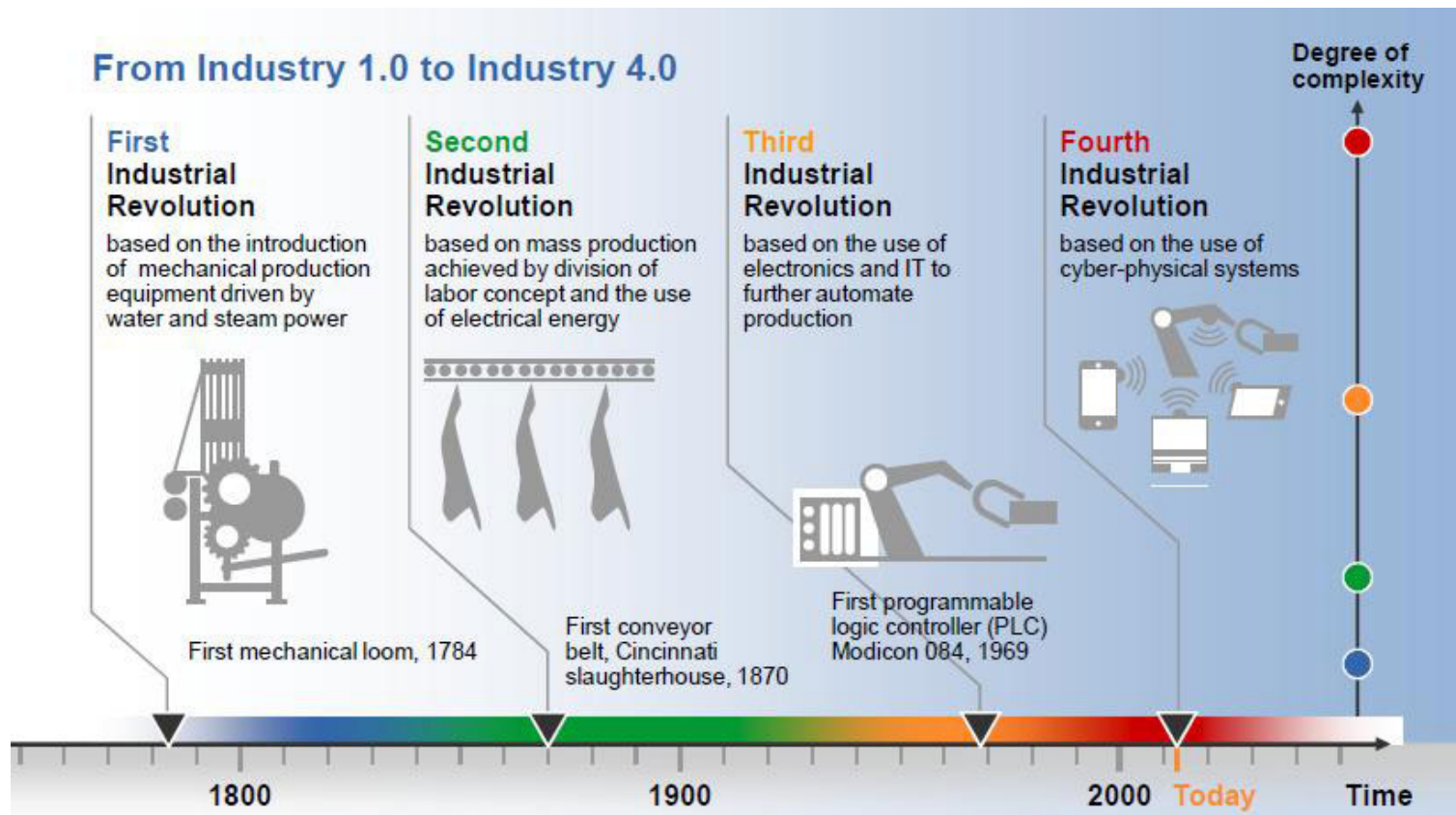
Source: urvil.wordpress.com

Automatic control of
Industrial Processes:

- Manufacturing plants
- Power plants
- Public transportation infrastructure
- Utility infrastructure (water treatment, gas/oil, power generation)

Industrial Control Systems

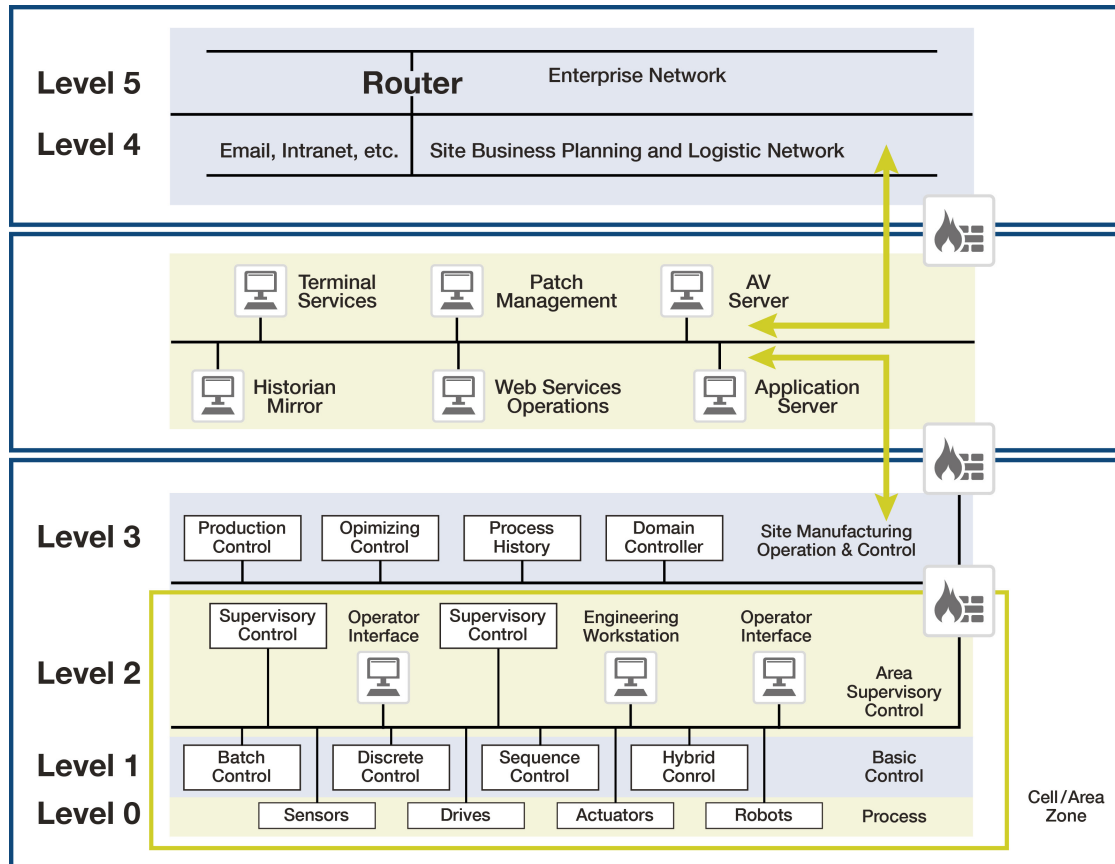
Industry Evolution



Source: <http://bcmpublicrelations.com/>

Industrial Control Systems

IT meets OT (Purdue Model)



Information Technology:
Servers and Client PCs

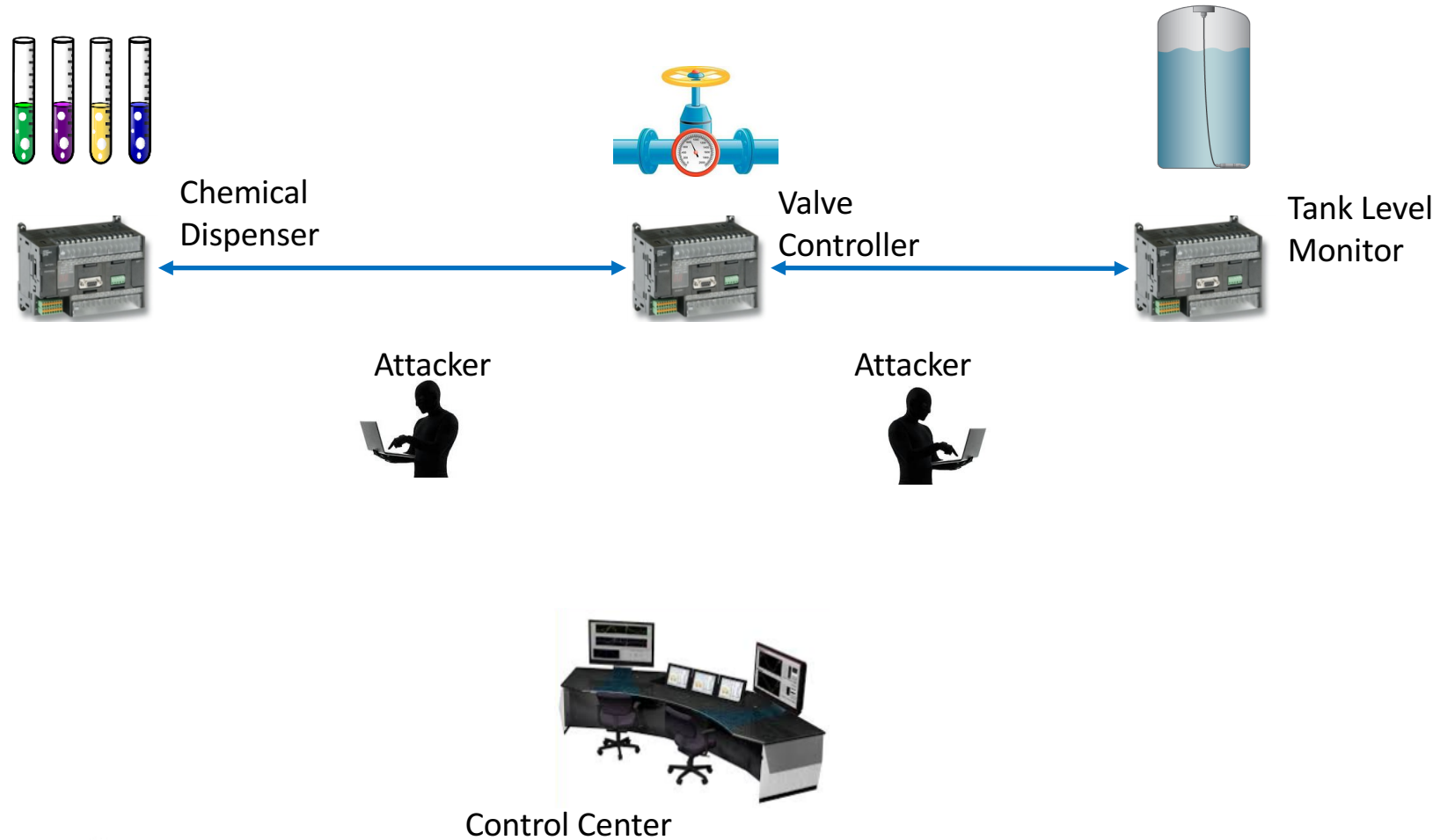
Operational Technology:
Servers, PLCs, SCADA, HMI
Devices, Actuators and
Sensors

Integrity Attacks cause
Operational Changes

Source: <https://pgionline.com/>

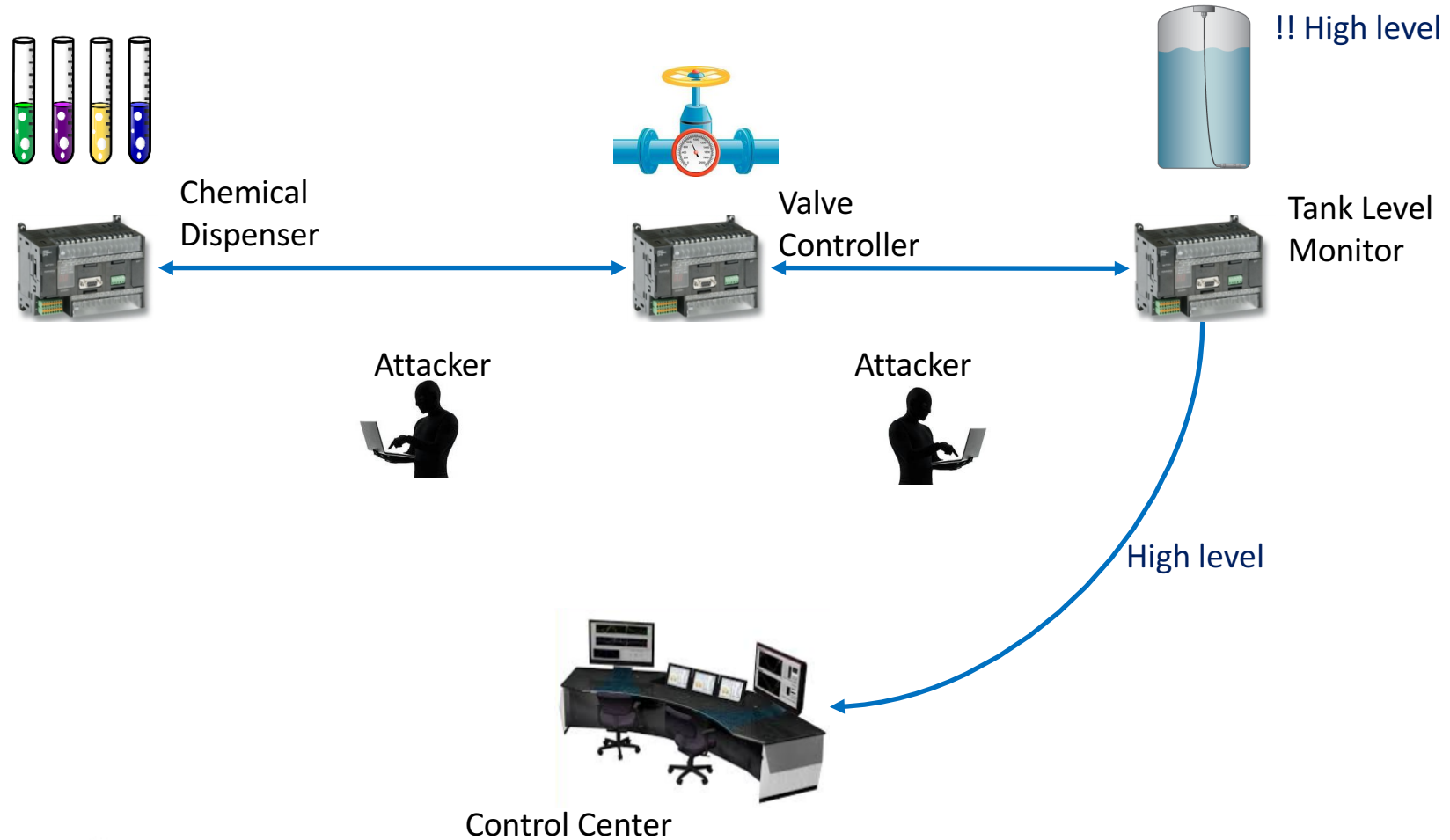
Cyber-security in ICS

Motivation: Integrity Attacks



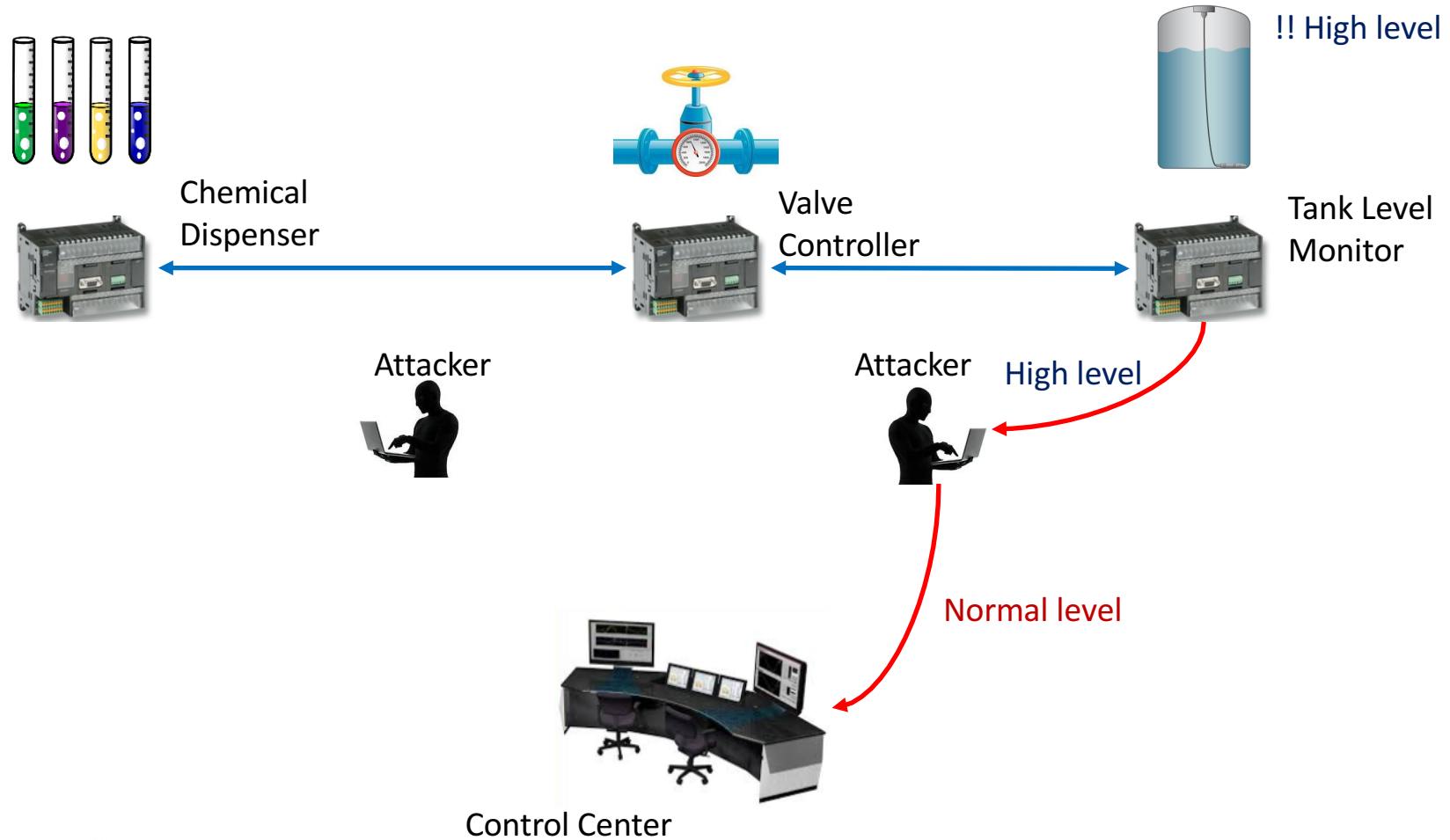
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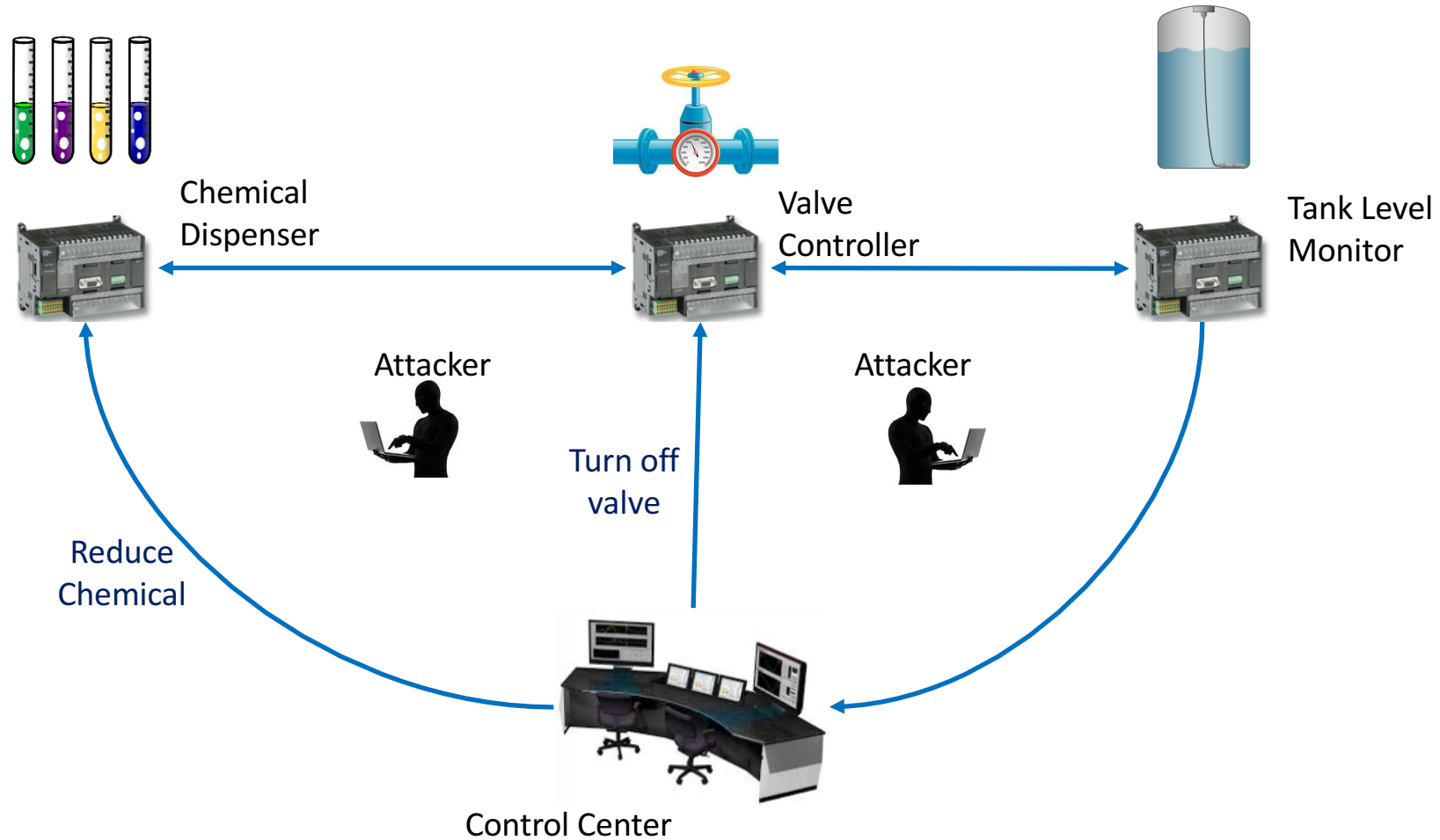
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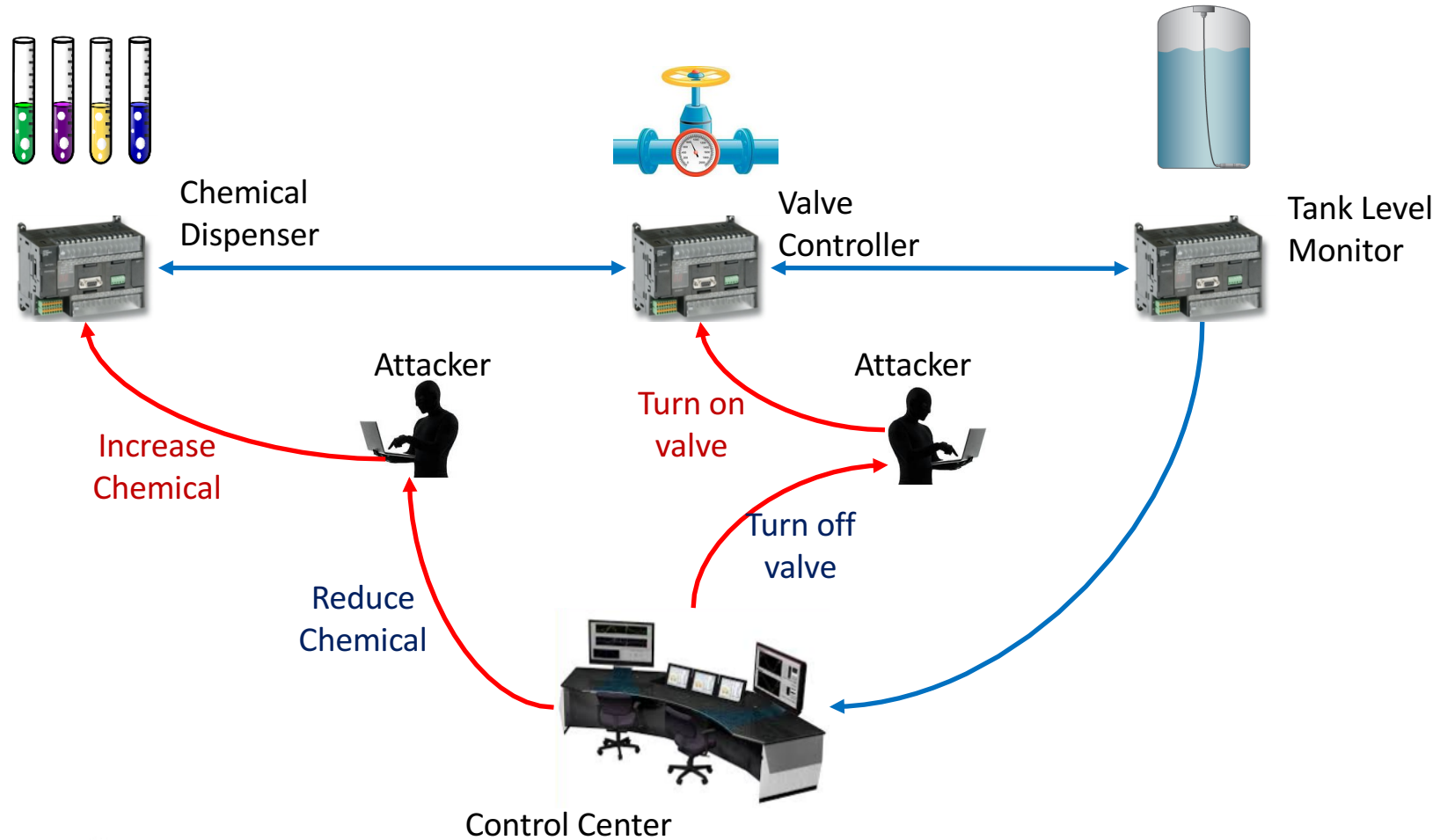
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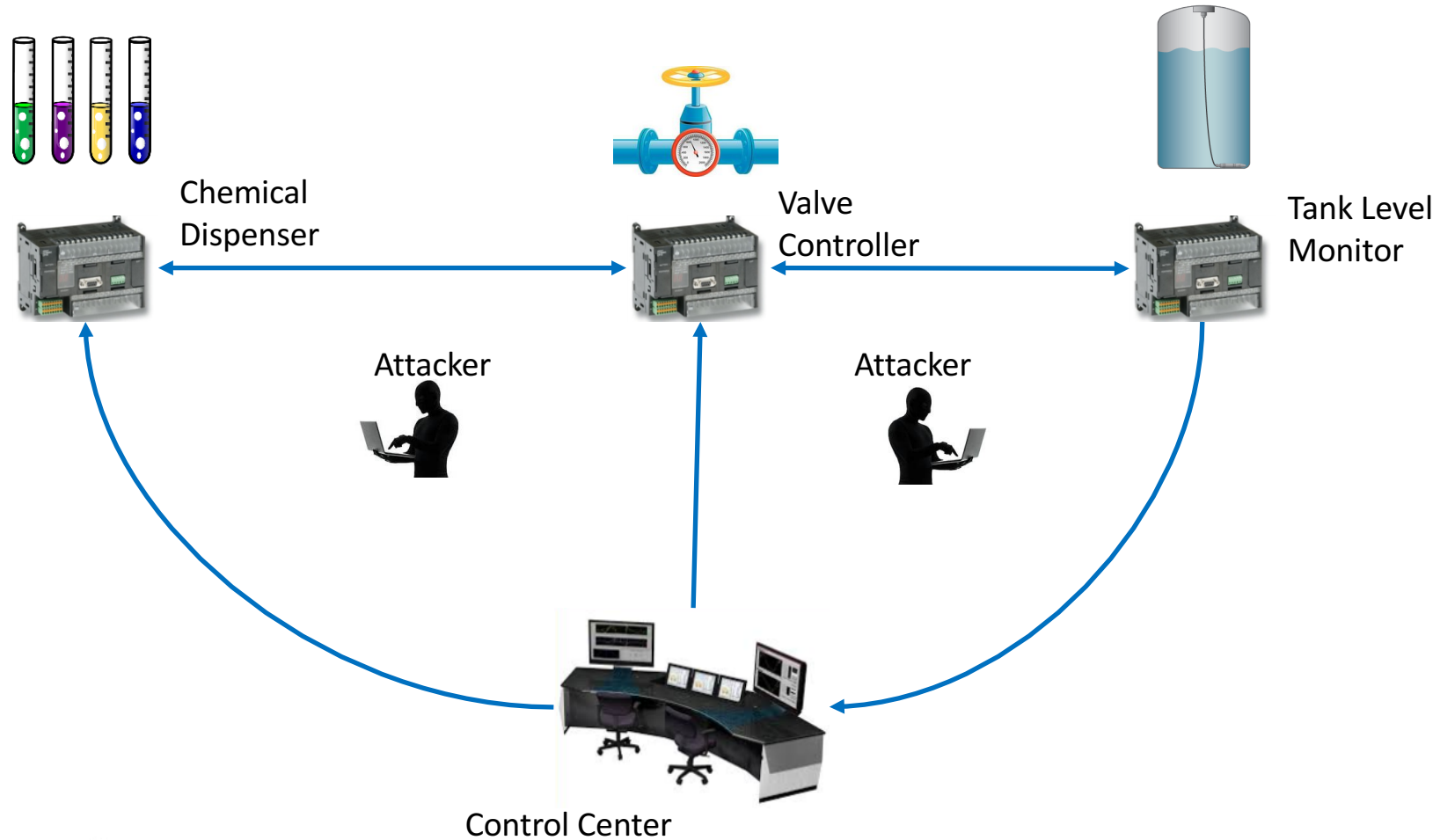
Cyber-security in ICS

Motivation: Integrity Attacks



Cyber-security in ICS

Motivation: Integrity Attacks



Countermeasures

Authenticity & Integrity checks



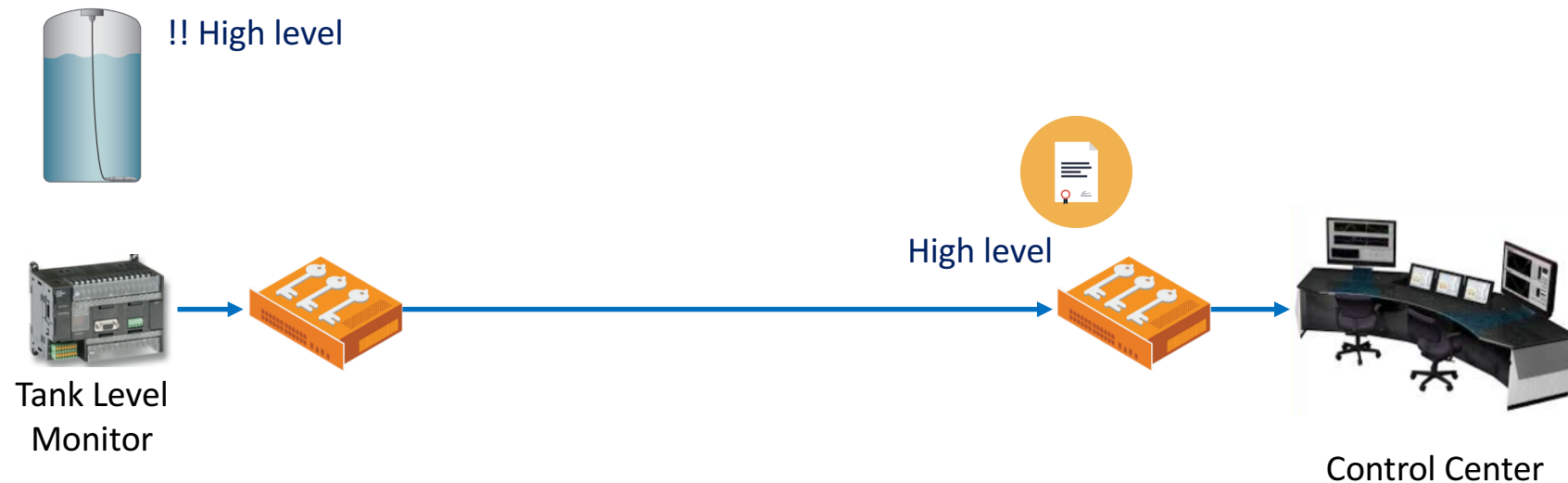
Countermeasures

Authenticity & Integrity checks



Countermeasures

Authenticity & Integrity checks



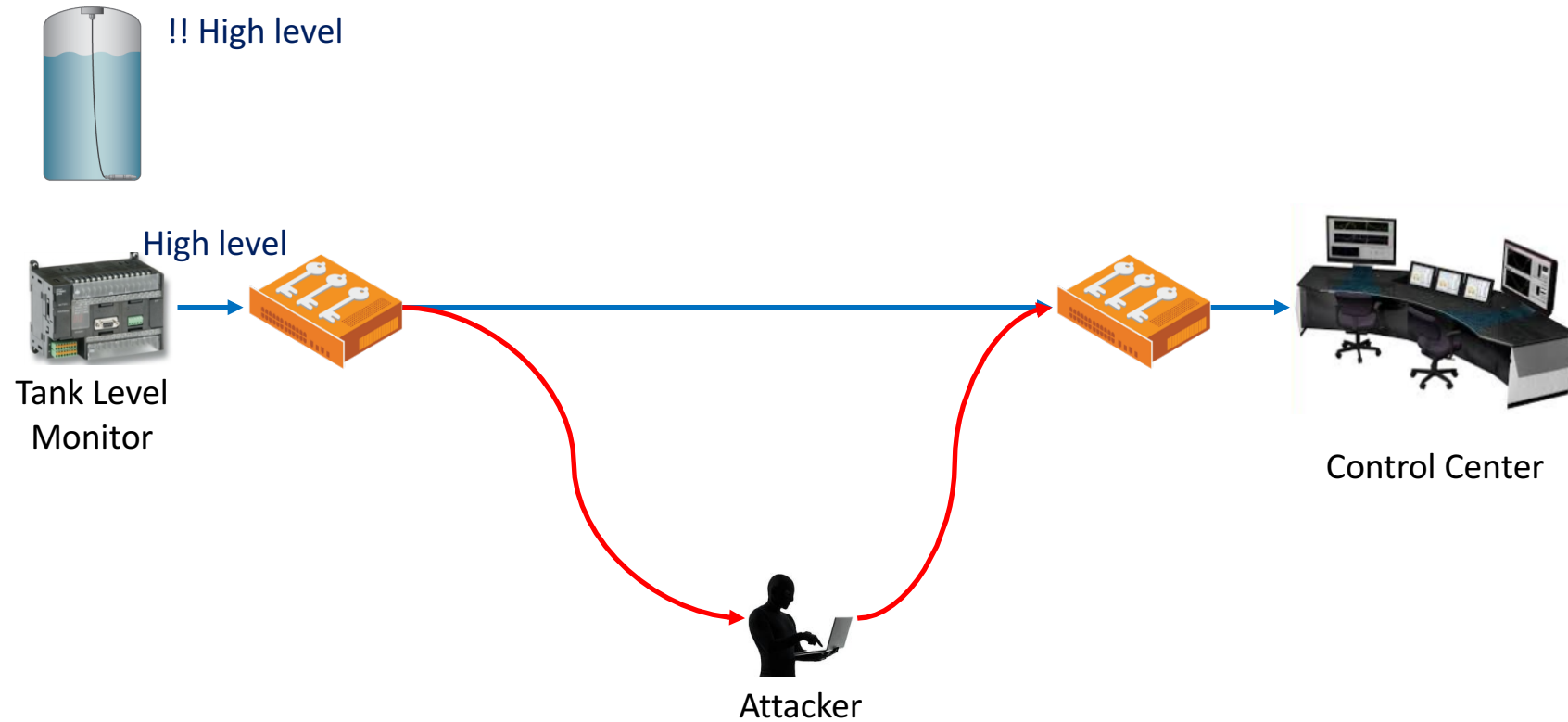
Countermeasures

Authenticity & Integrity checks



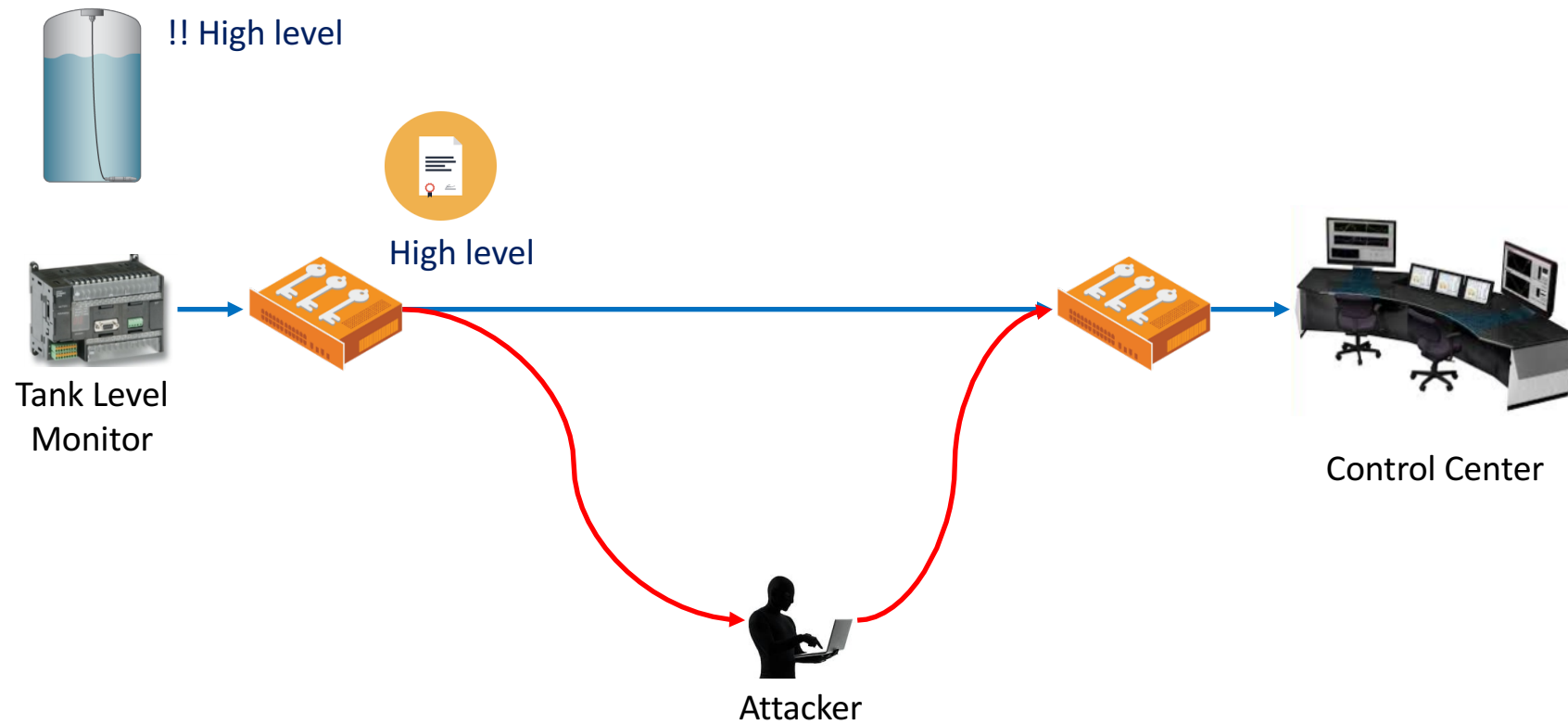
Countermeasures

Authenticity & Integrity checks



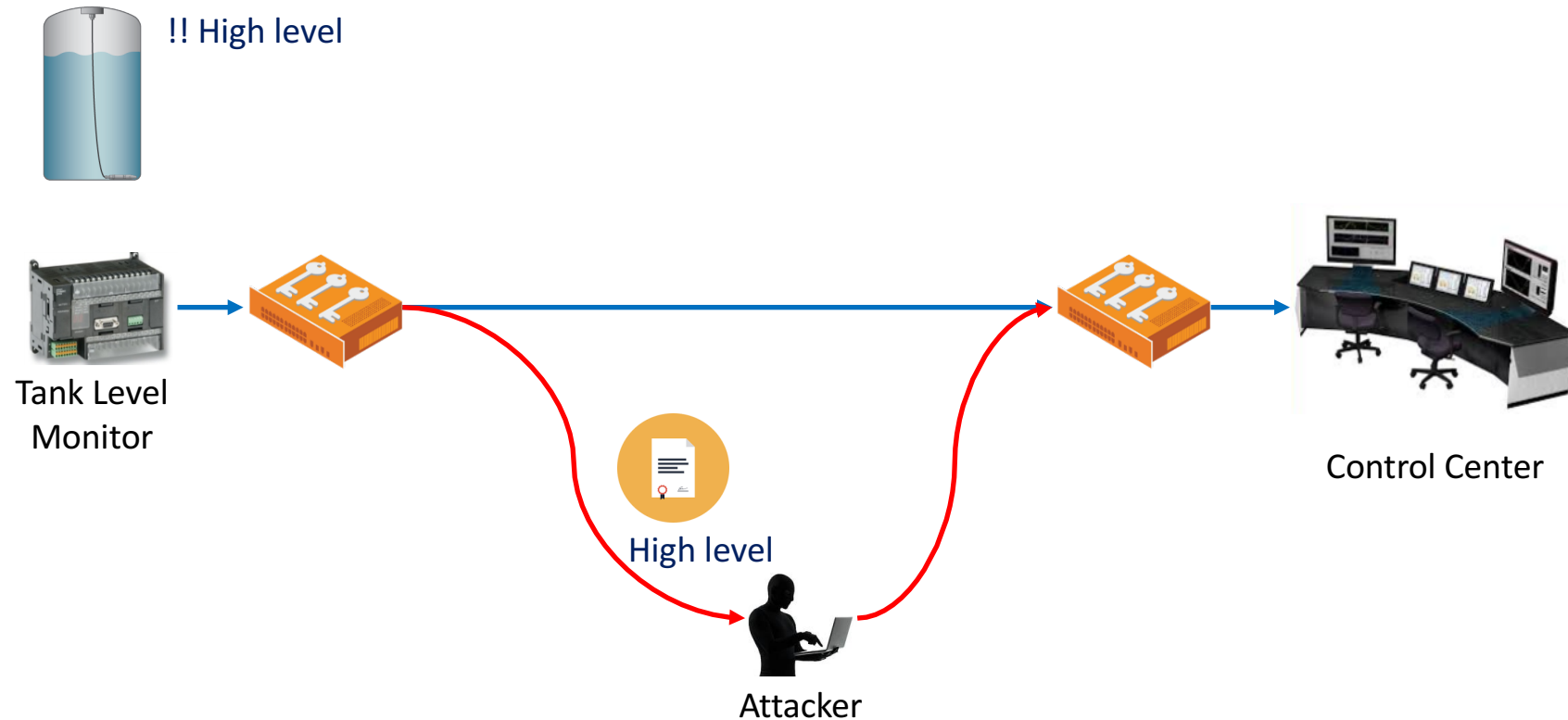
Countermeasures

Authenticity & Integrity checks



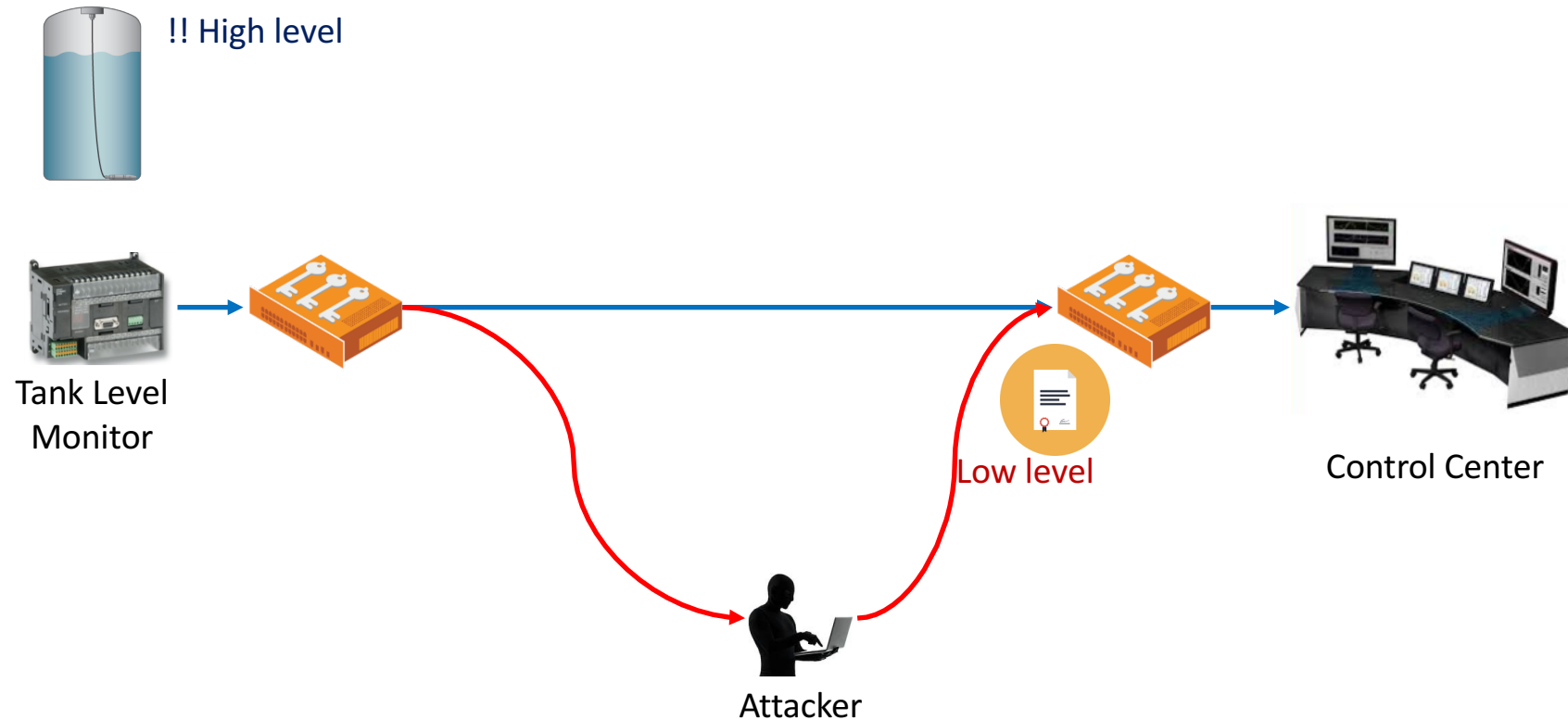
Countermeasures

Authenticity & Integrity checks



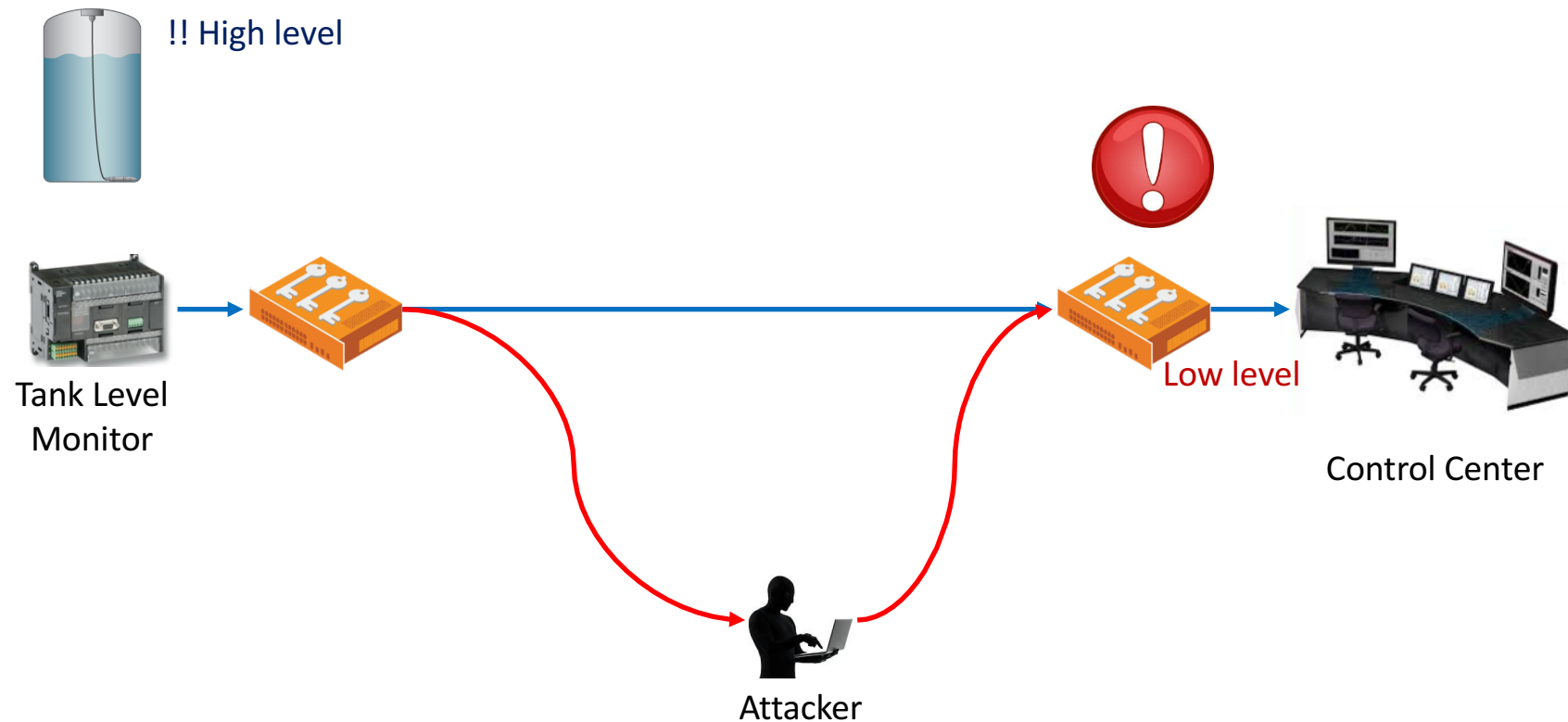
Countermeasures

Authenticity & Integrity checks



Countermeasures

Authenticity & Integrity checks



Industrial Control Systems

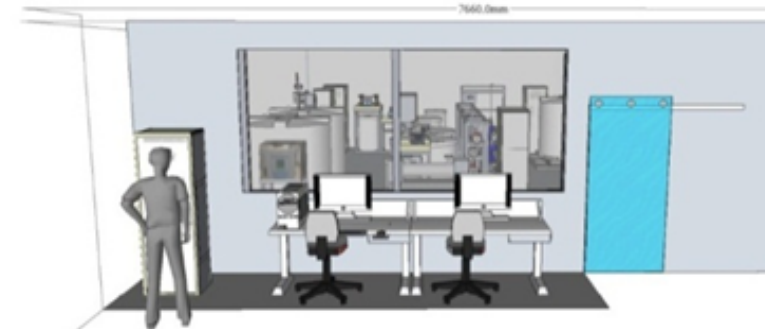
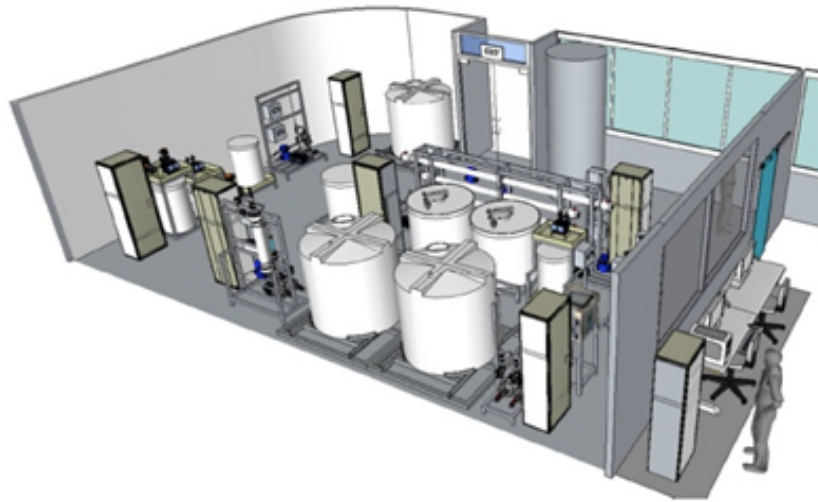
IT/OT Requirements

Attribute	Information Technology Systems (IT)	Industrial Control Systems (OT)
Component Lifetime	3 to 5 years	10 to 15 years
Connectivity	Corporate network, IP-based, standard protocols	Control Network, proprietary protocols
Performance Requirements	Non-real-time	Real-time

Sources:
NIST: Guide to Industrial Control Systems Security. 800-82 Rev2
<http://www.wbdg.org/>

Data from a real ICS

SWaT Testbed



Secure Water Treatment (SWaT) is a testbed for research in the area of cyber security.



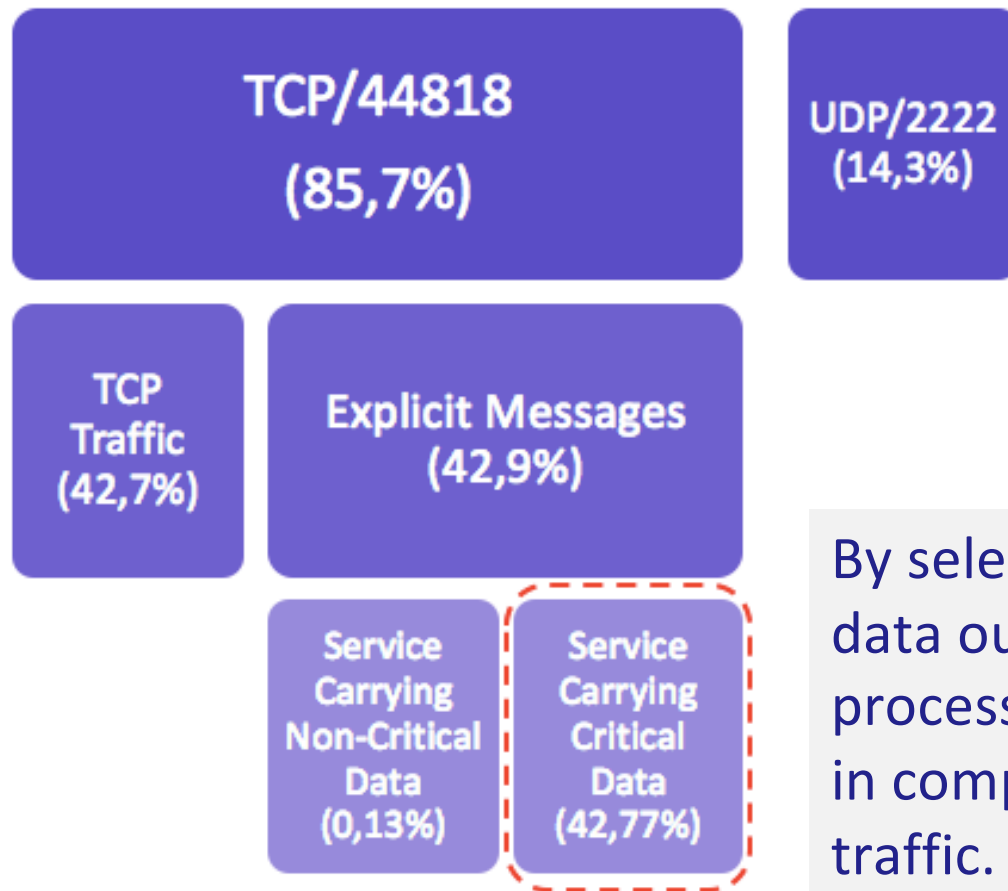
Data from a real ICS

Real-time requirements

		Sent	Received
CIP Message Type	REQUEST	561 Pk/s Size ($\mu=63\text{B}$, $\sigma=3.36$)	607 Pk/s Size ($\mu=69\text{B}$, $\sigma=5.32$)
	RESPONSE	566 Pk/s Size ($\mu=75\text{B}$, $\sigma=58.16$)	561 Pk/s Size ($\mu=86\text{B}$, $\sigma=9.42$)
	TOTAL	1127 Pk/s (Required Signing Performance)	1168 Pk/s (Required Verifying Performance)

Data from a real ICS

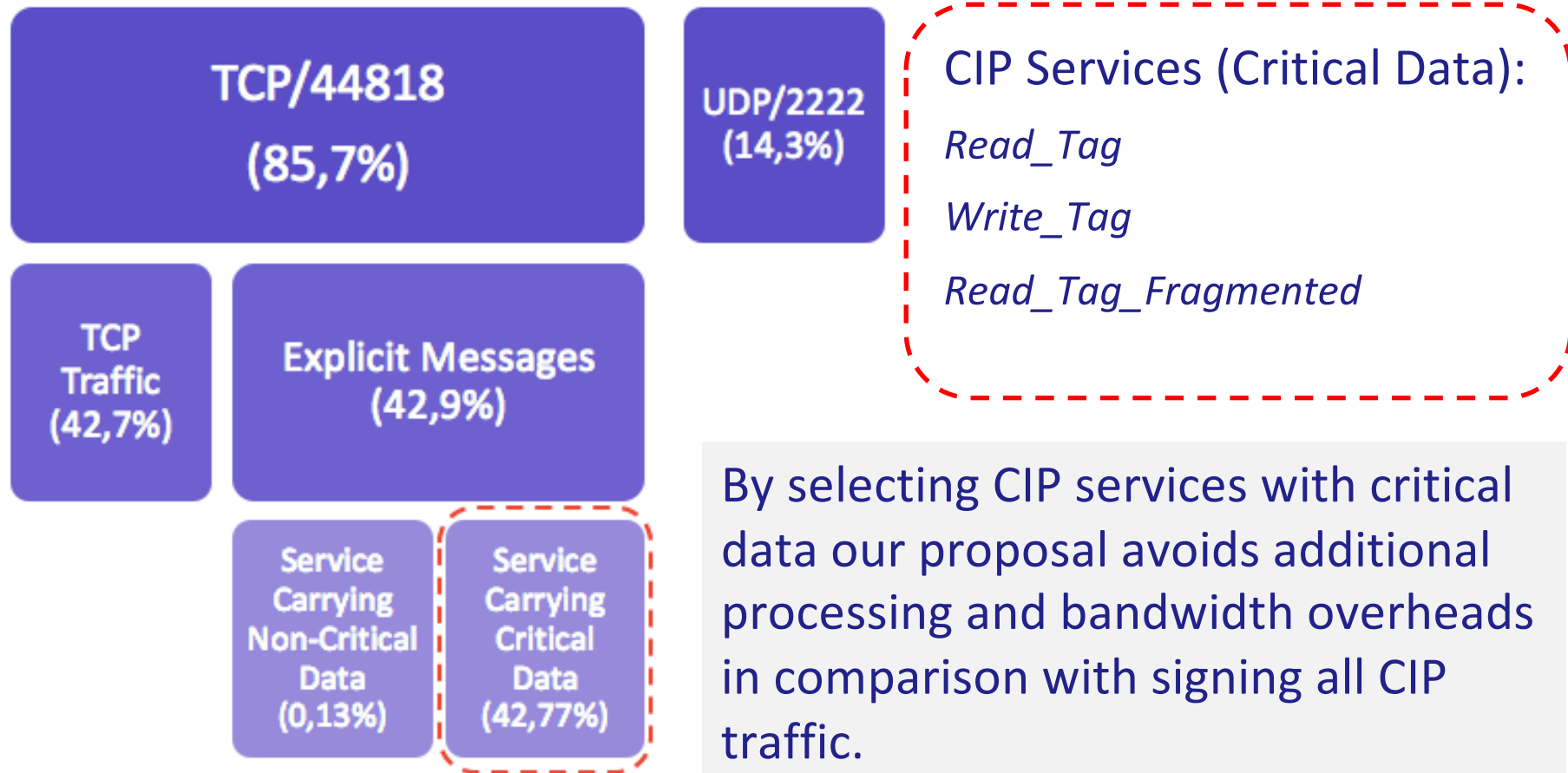
Understanding ICS Data



By selecting CIP services with critical data our proposal avoids additional processing and bandwidth overheads in comparison with signing all CIP traffic.

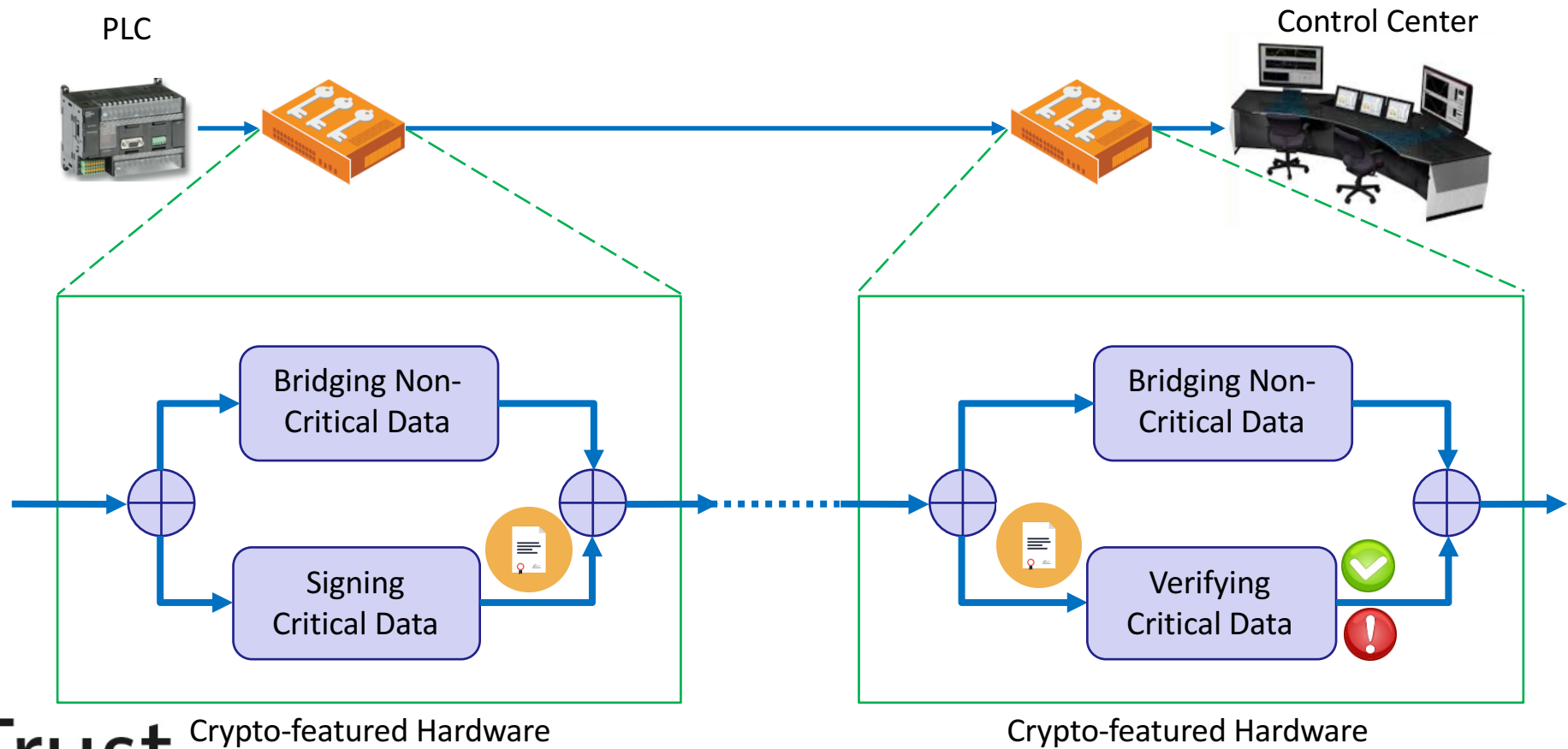
Data from a real ICS

Understanding ICS Data



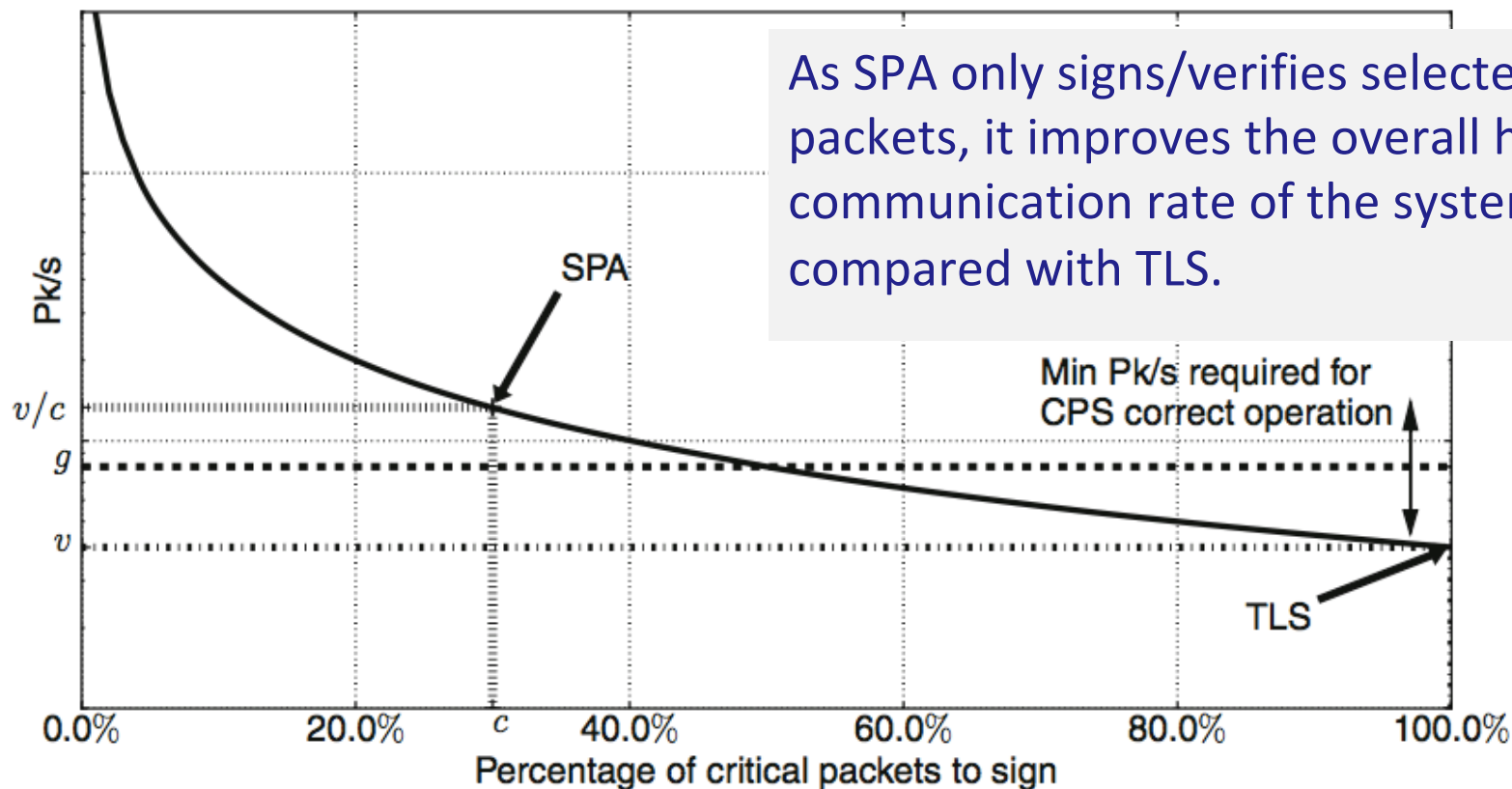
SPA Protocol

Selective Packet Authentication



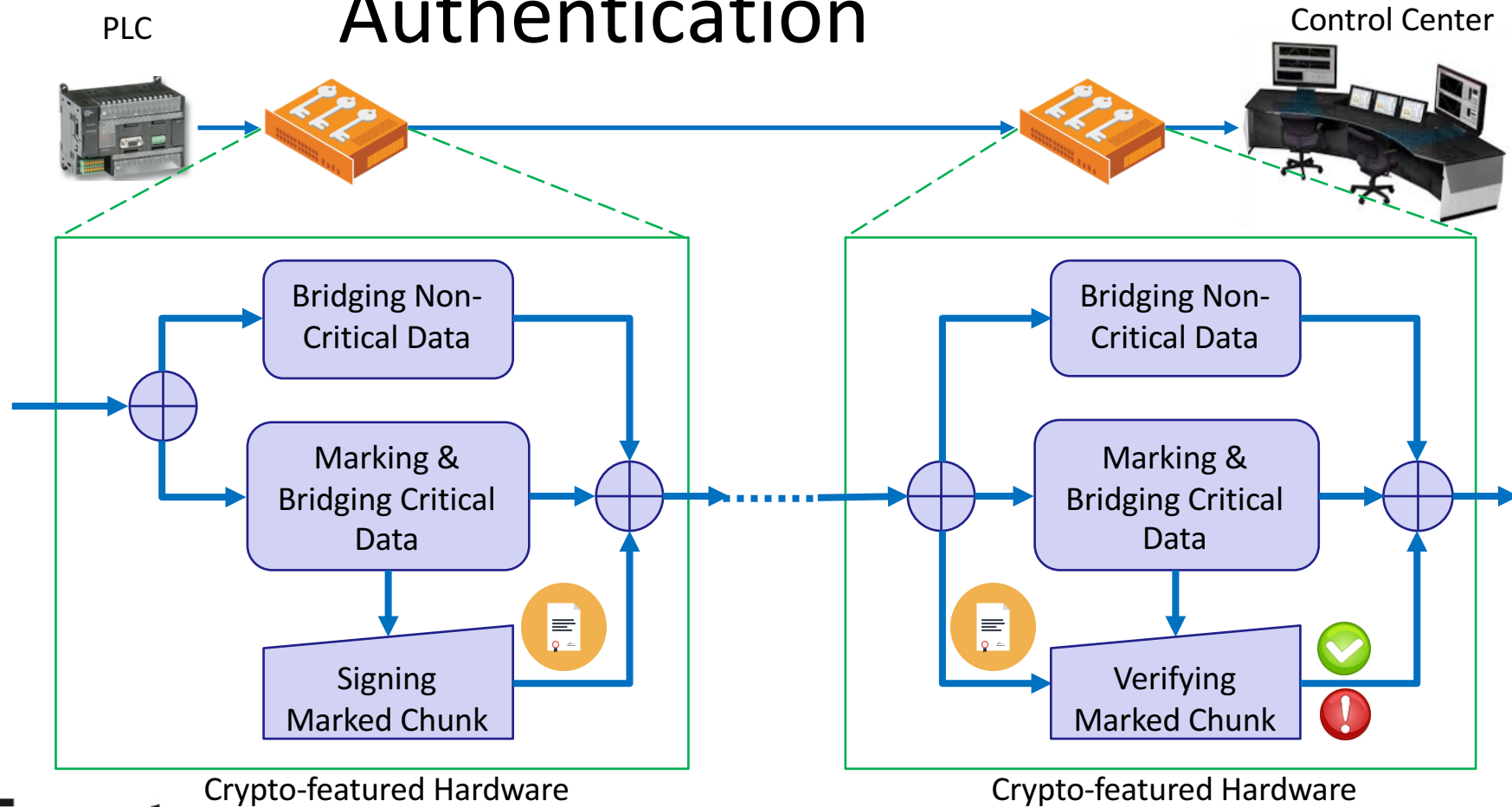
Comparison with TLS

SPA Evaluation



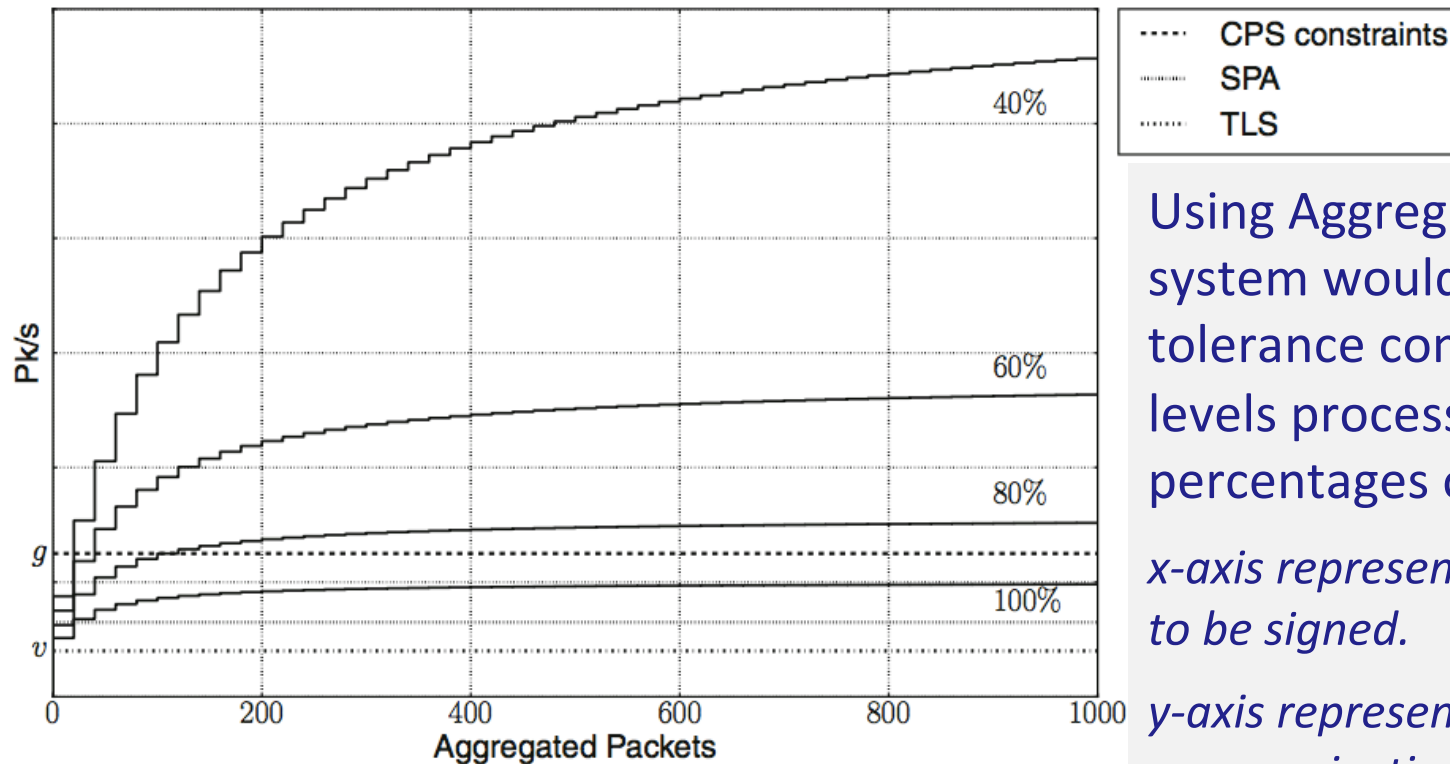
ASPA Protocol

Aggregated Selective Packet Authentication



Comparison with TLS

ASPA Evaluation



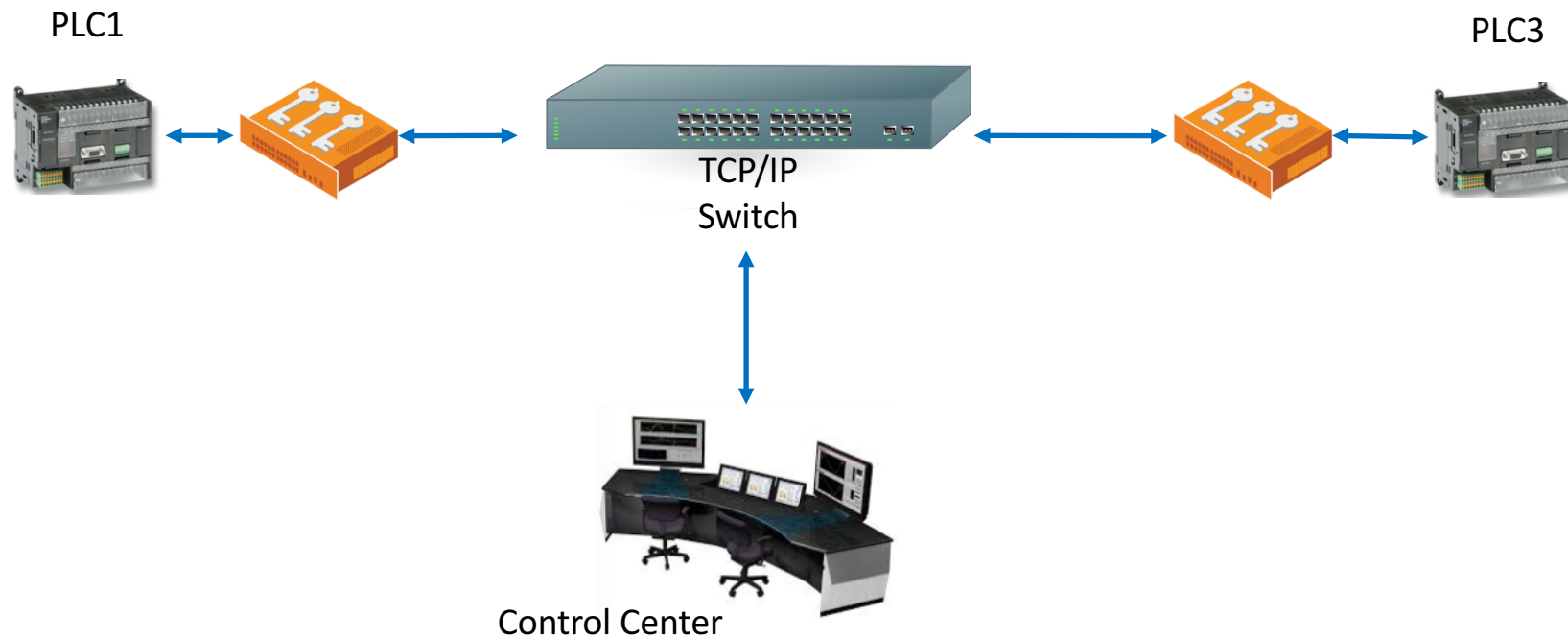
Using Aggregated-SPA the system would achieve higher tolerance communication levels processing different percentages of critical data.

x-axis represents chunk of packets to be signed.

y-axis represents tolerance at communication level reached by the system.

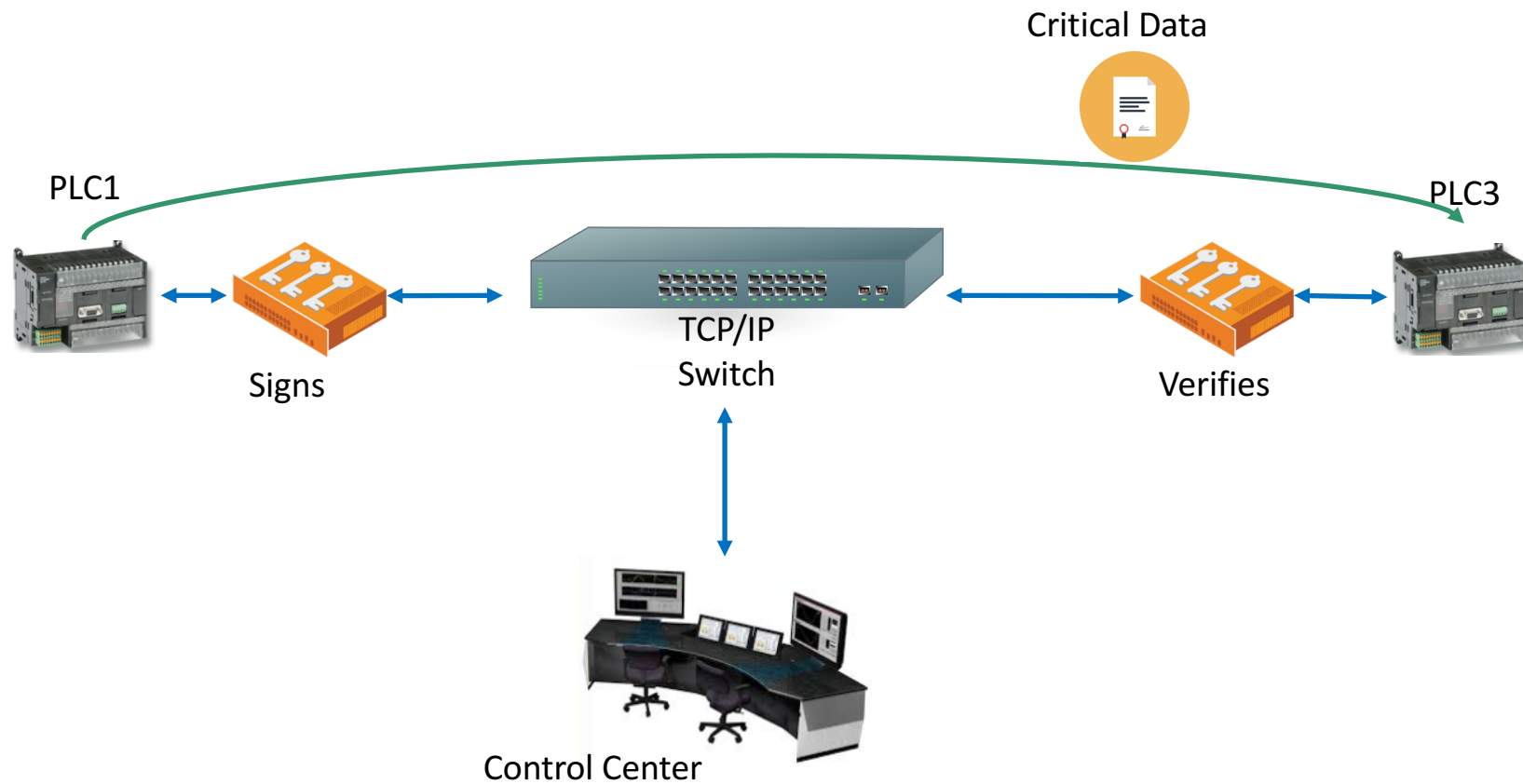
Implementation

Real Scenario on SWaT Testbed



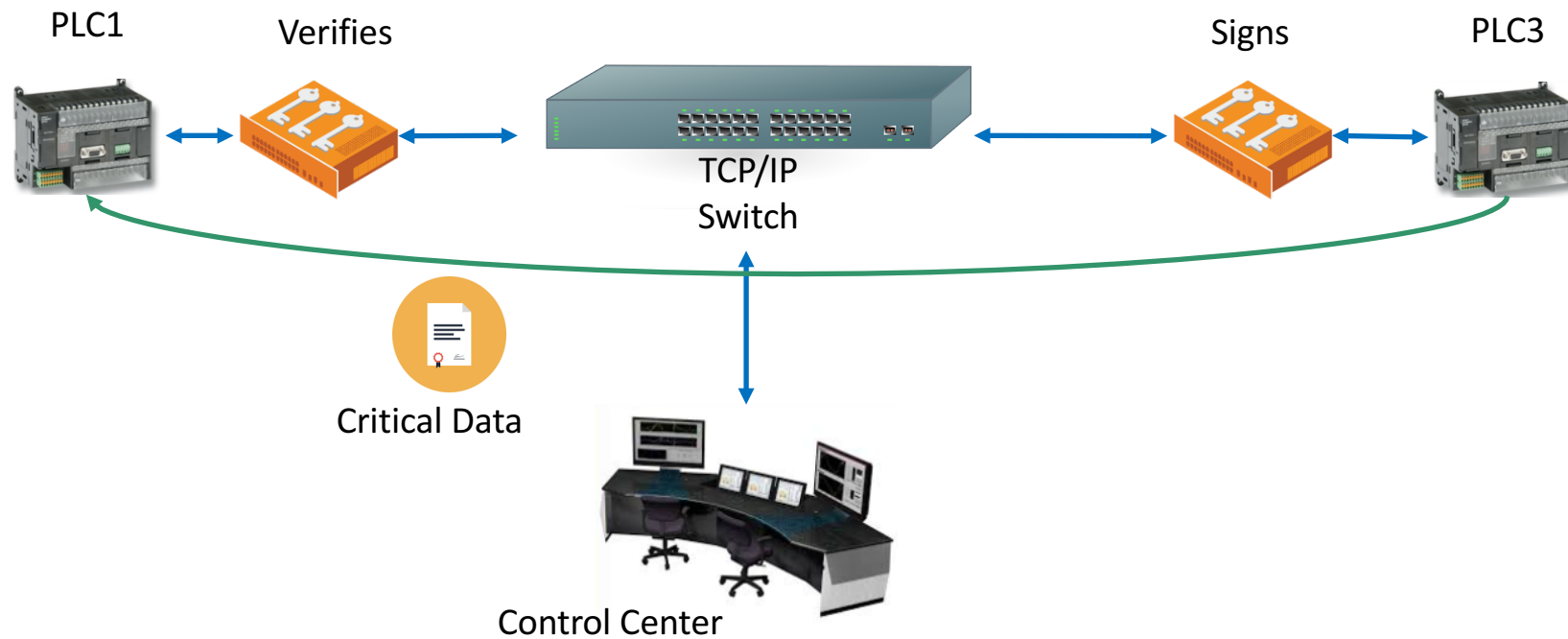
Implementation

Real Scenario on SWaT Testbed



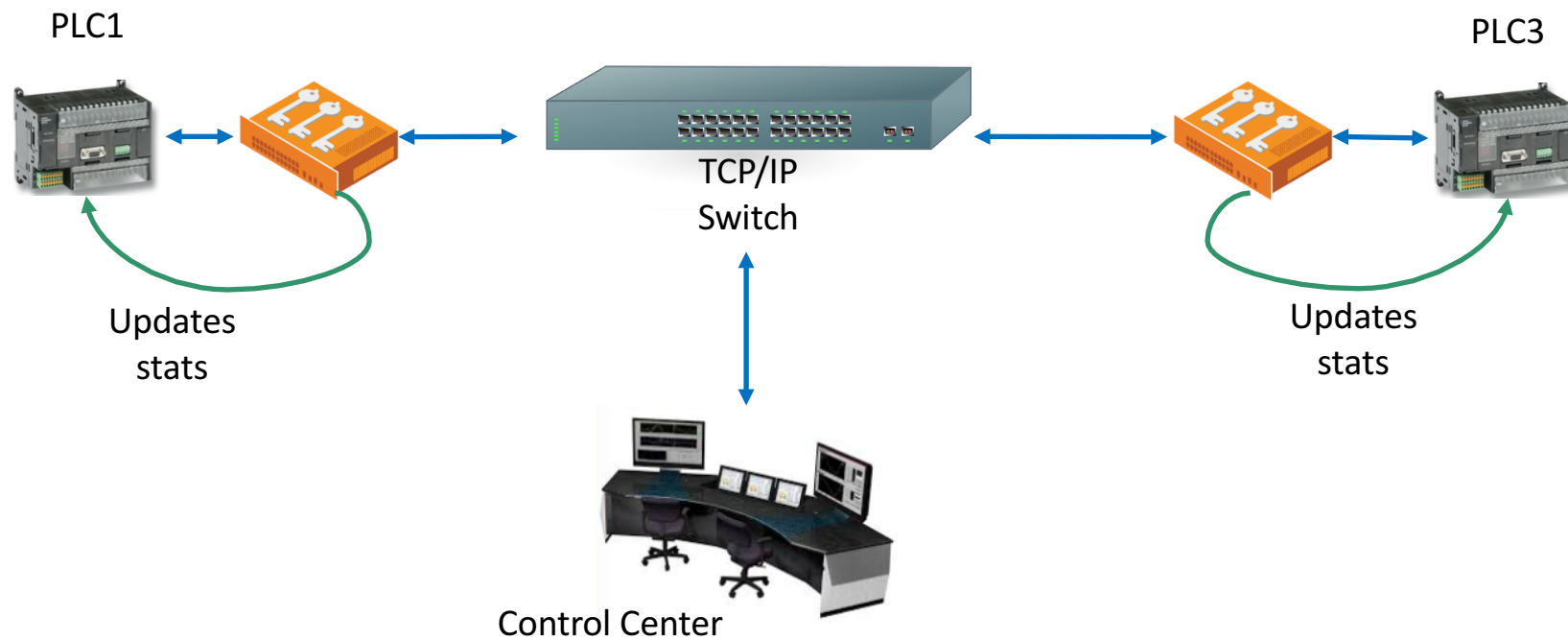
Implementation

Real Scenario on SWaT Testbed



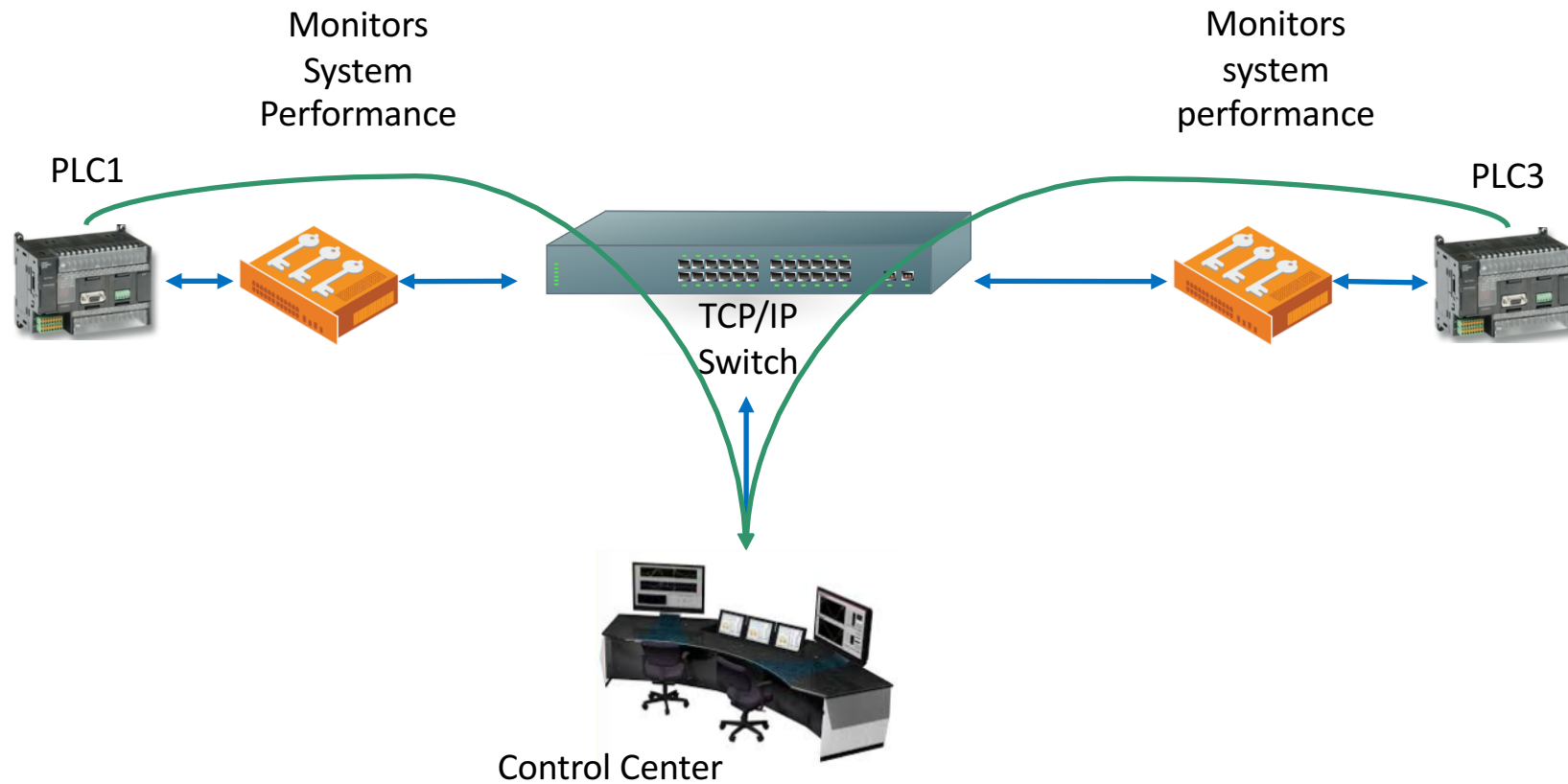
Implementation

Real Scenario on SWaT Testbed



Implementation

Real Scenario on SWaT Testbed



Benchmark

Hardware Selection

Hardware	Processor	CPU	Memory
Controllino	ATmega2560 Microcontroller	16 MHz	256 KB
ARM (VM*)	ARM926EJ-S	540 MHz	256 MB
Raspberry PI 2	Quad-core ARM Cortex-A7	900 MHz	1 GB
Raspberry PI 3	Quad-core ARM Cortex-A53	1200 MHz	1 GB
PC (VM*)	Intel Core i5-5300 U	2300 MHz	2 GB

*VM: Virtual Machine

Benchmark

Hardware Performance

Data Size (Bytes)	Controllino	ARM	Raspberry PI2	Raspberry PI3	PC
64	2.2×10^4	76	53	15	2
128	3.3×10^4	78	58	16	2
256	5.5×10^4	84	69	18	3
512	1×10^5	117	89	32	4
1K	1.8×10^5	171	130	35	6
2K	3.6×10^5	252	211	58	10
4K	7×10^5	474	374	104	18
ECDSA	N/A	1.5×10^5	1×10^5	3.2×10^4	3.1×10^3

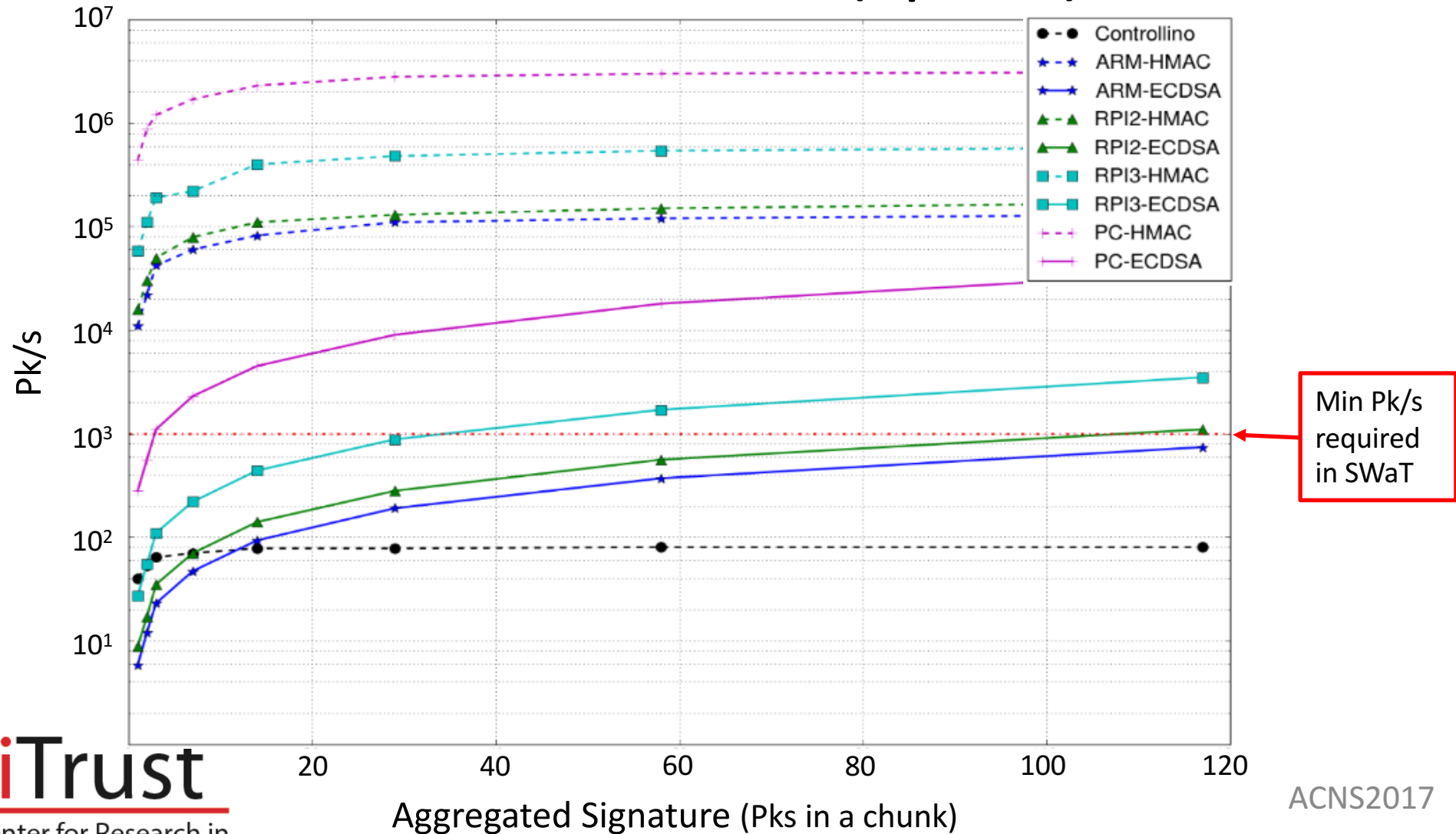
All data in μ s

Cryptographic Algorithms:

- Symmetric: HMAC-SHA256
- Asymmetric: ECDSA

ASPA Protocol

Performance Evaluation (Speed)



Conclusions

- Our protocols are backward compatible, as they transmit authentication data as payload in legacy industrial protocols.
- With inexpensive and fast hardware (Raspberry PI), it is feasible to enhance legacy plants with authentic channels for strong signature algorithms with simple protocols.
- It is feasible to significantly raise the bar against attackers of ICS by including authentication based on modern cryptography without compromising efficiency or cost.
- We plan to compare the real-time constraints of SWaT with constraints in other ICS Testbeds (Smart Grid).

Thank you

Q & A

Backup Slides

Industrial Control Systems

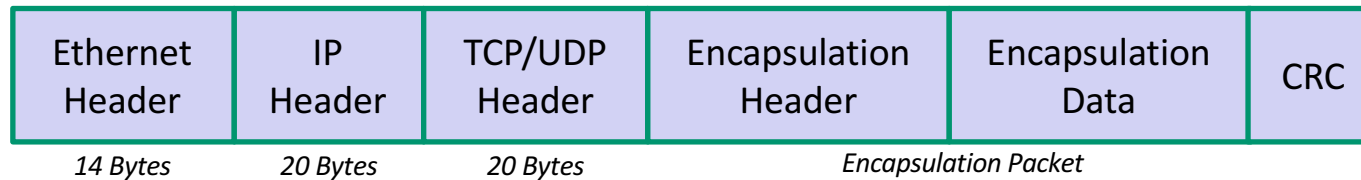
IT/OT Requirements

Attribute	Information Technology Systems (IT)	Industrial Control Systems (OT)
Purpose	Process transaction, provide information	Controls and monitor physical processes
Role	Support people	Control machines
Architecture	Enterprise wide infrastructure and applications	Event-driven, real-time, embedded hardware and customized software
Component Lifetime	3 to 5 years	10 to 15 years
Interfaces	GUI, Web browser, terminal and keyboard	Electromechanical, sensors, actuators, coded displays
Connectivity	Corporate network, IP-based, standard protocols	Control Network, proprietary protocols
Performance Requirements	Non-real-time	Real-time
Major risk impacts	Delay of business operations	Environmental impacts, loss of life, equipment, or production

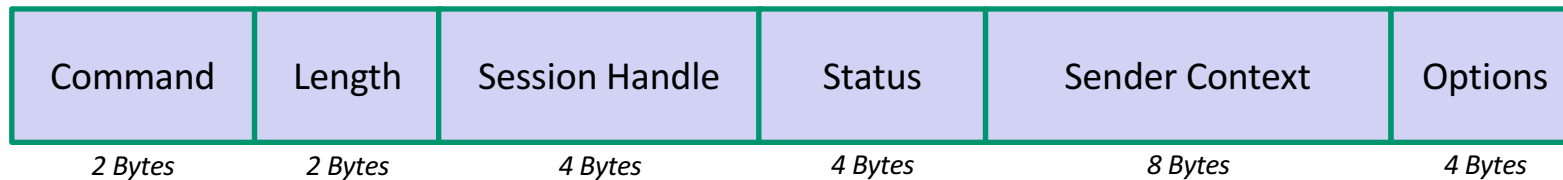
Sources:
NIST: Guide to Industrial Control Systems Security. 800-82 Rev2
<http://www.wbdg.org/>

Injecting data into Ethernet IP Protocol

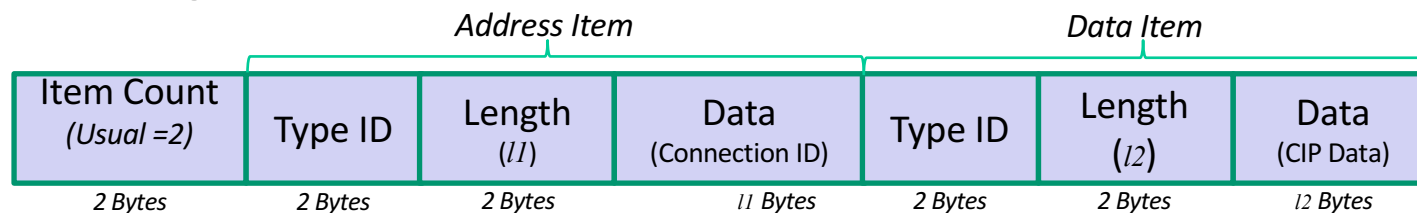
Ethernet Frame



Encapsulation Header

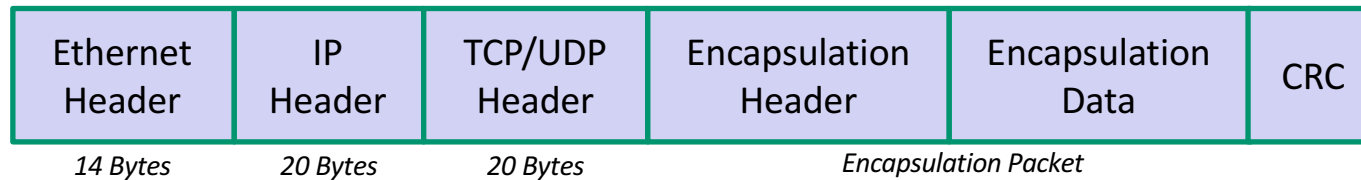


Encapsulation Data (Common Packet Format)

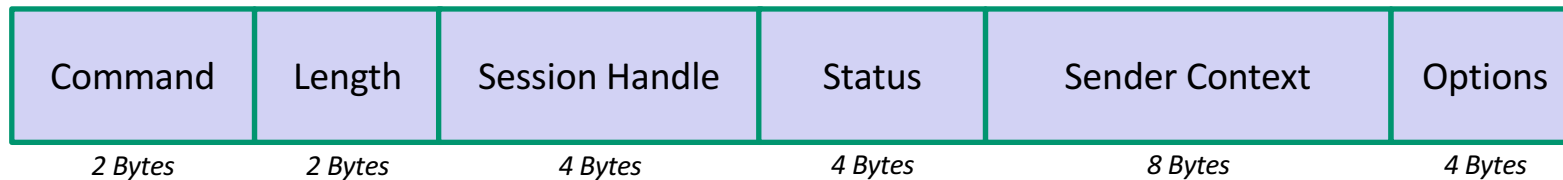


Injecting data into Ethernet IP Protocol

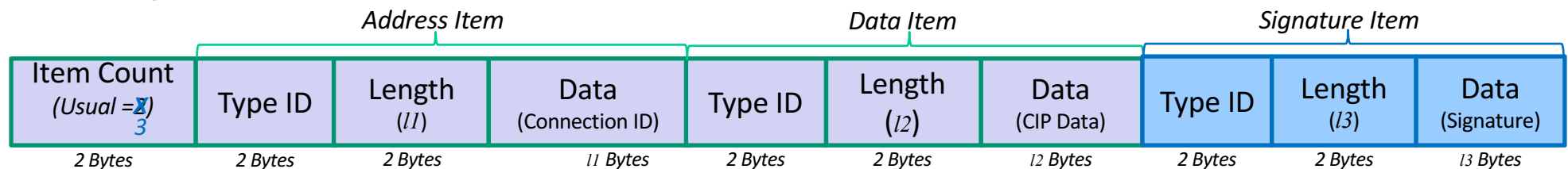
Ethernet Frame



Encapsulation Header



Encapsulation Data (Common Packet Format)

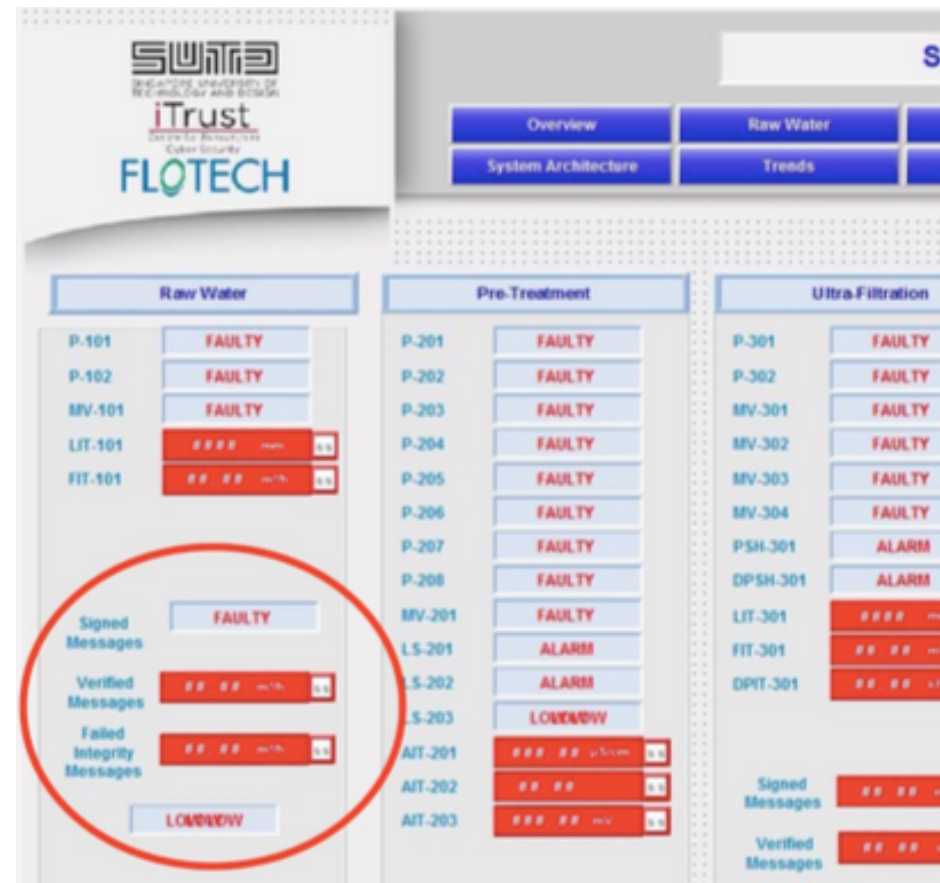


Authentication Protocols

Implementation:

Real Scenario on SWaT Testbed

- SCADA's supervisory reads PLC variables of signing-verification process.
- Statistics about integrity checks might be summarize.
- In case of integrity violations happen an alarm will trigger.



Implementation

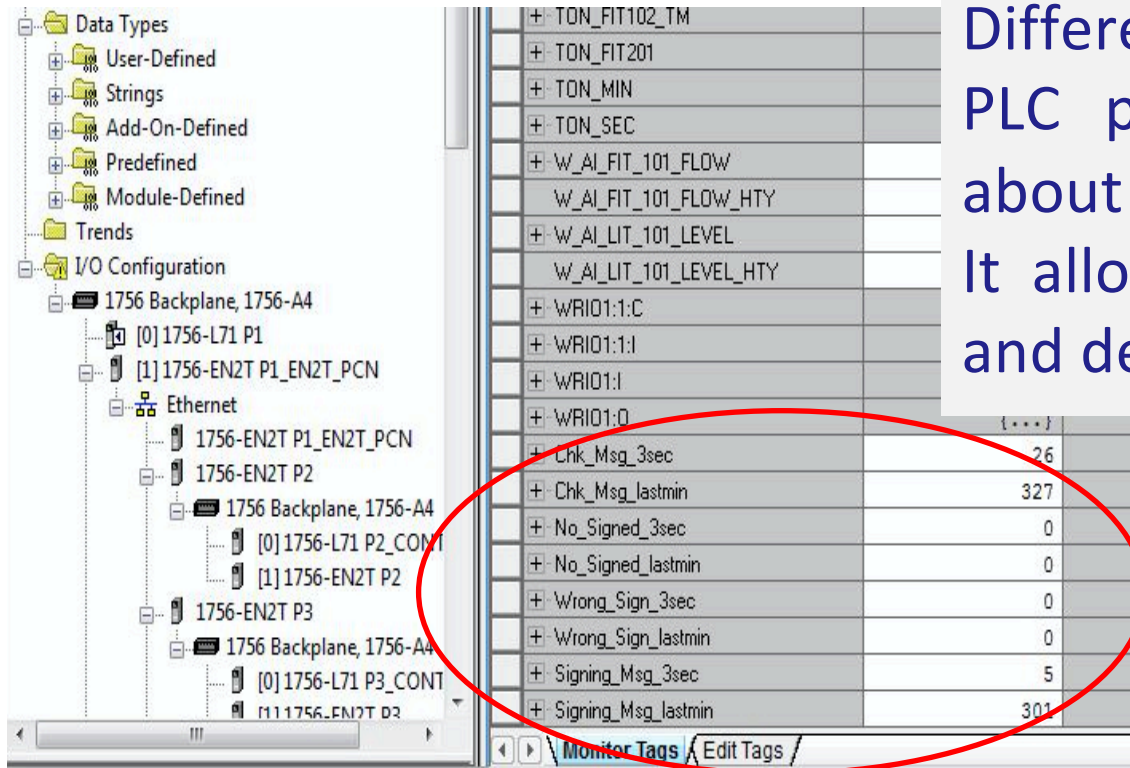
Real Scenario on SWaT Testbed

A Raspberry Pi is directly connected between the hardened PLC and its closest switch. It bridges communication between the PLC and the rest of the system.



Implementation

Real Scenario on SWaT Testbed



Different tags were configured at PLC program to store statistics about signing/verification process. It allows to monitor the process and debug it.